

FIG. 1

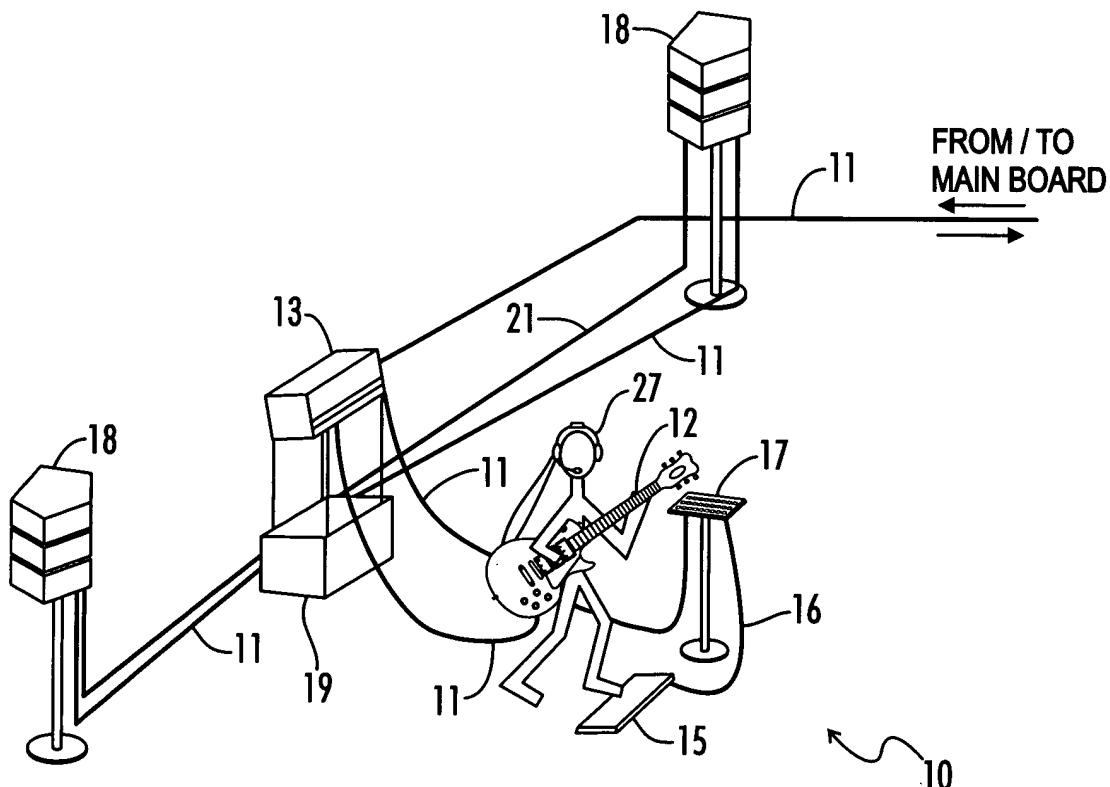


FIG. 2

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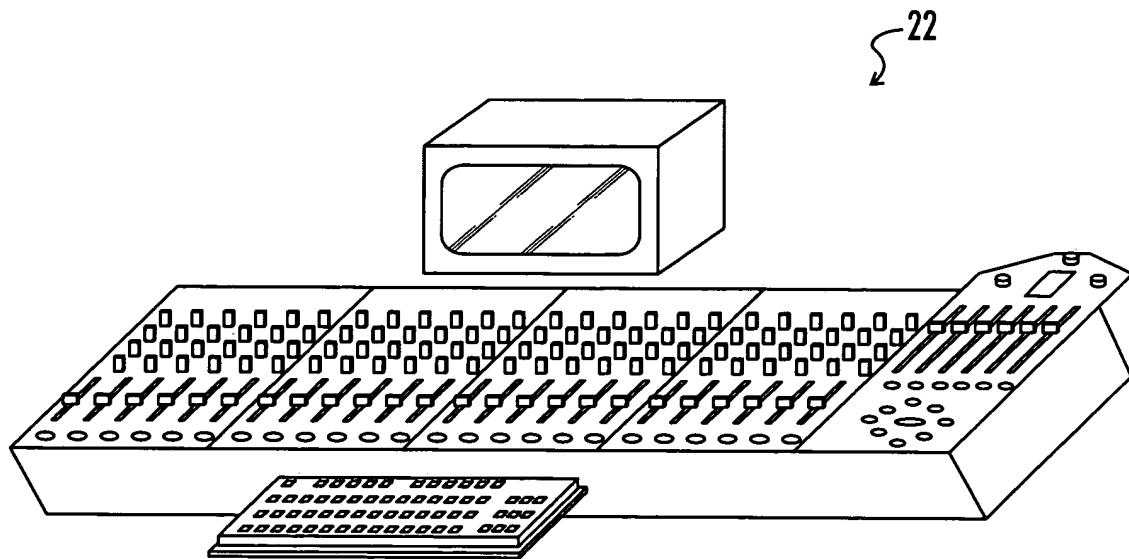


FIG. 3

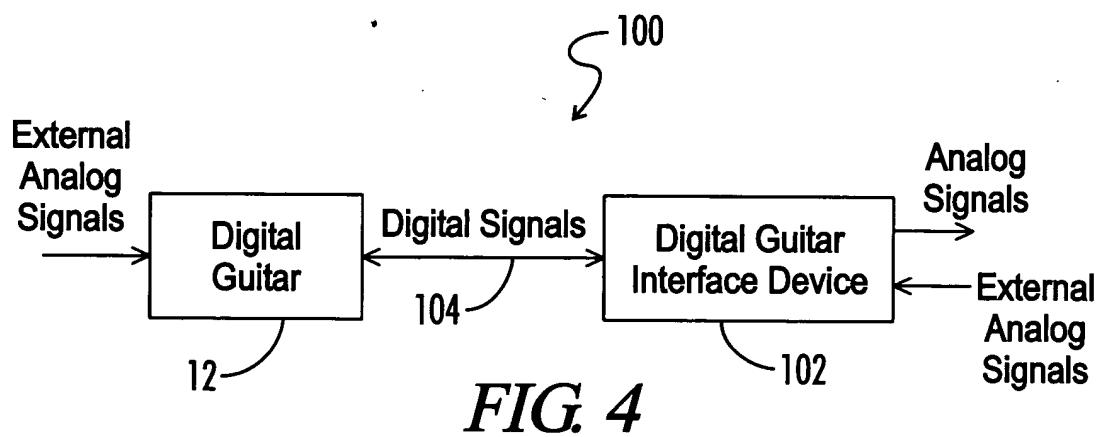


FIG. 4

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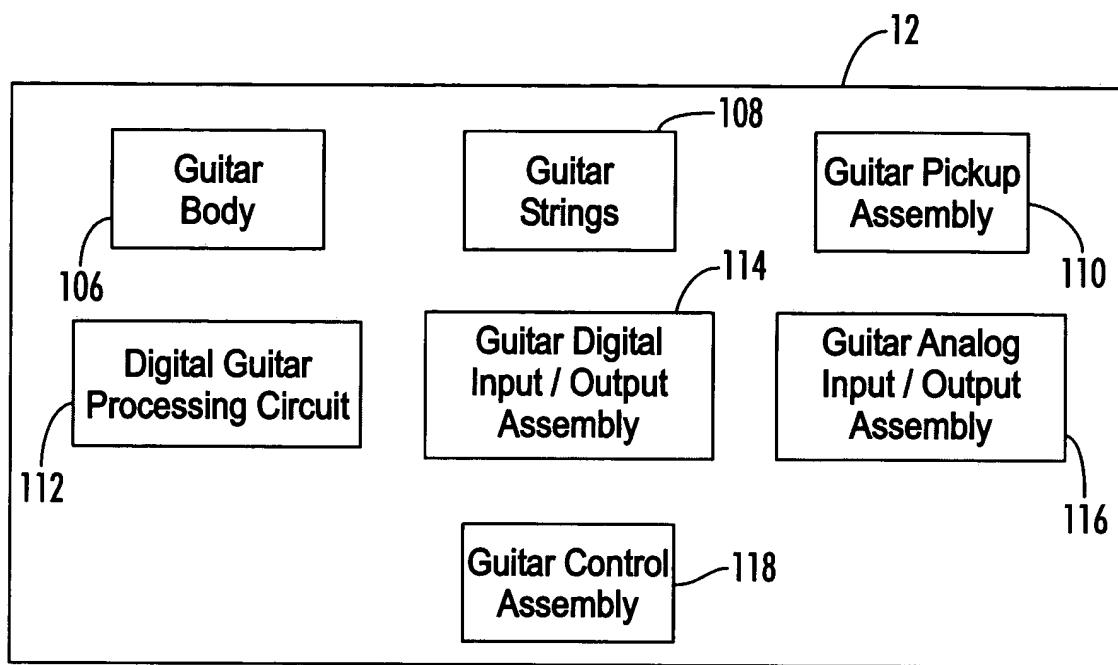


FIG. 5

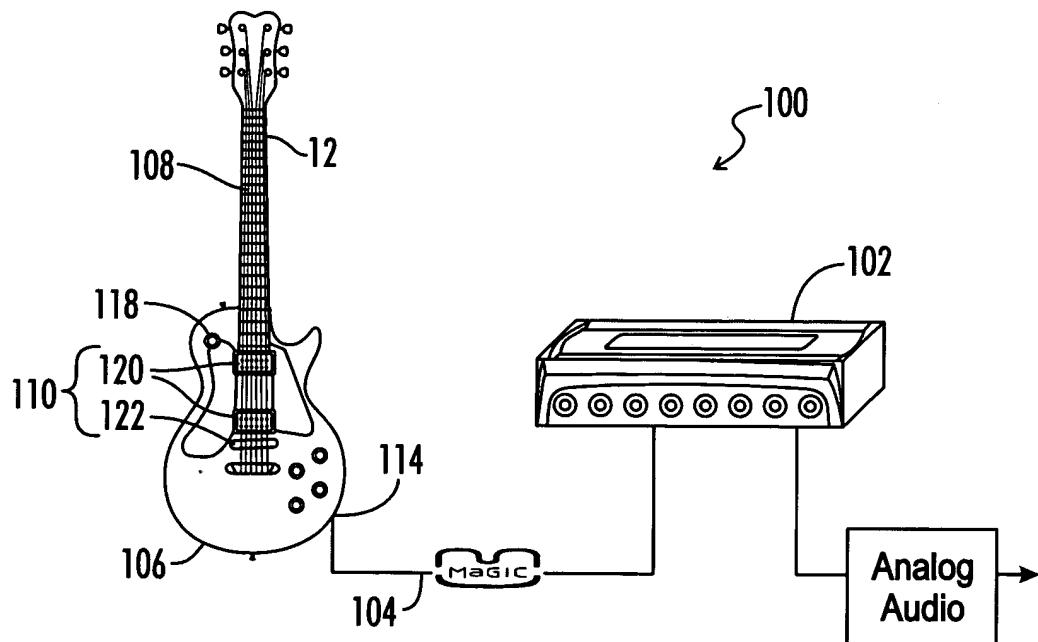


FIG. 6

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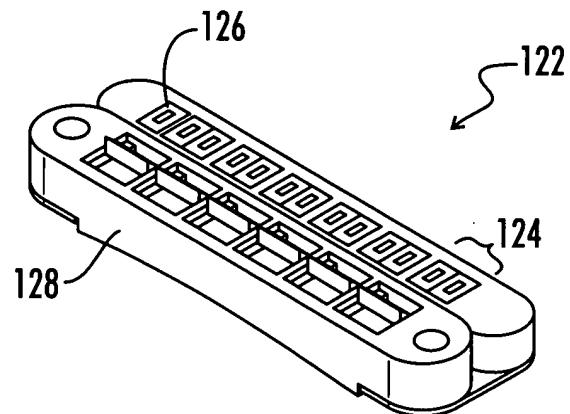


FIG. 7

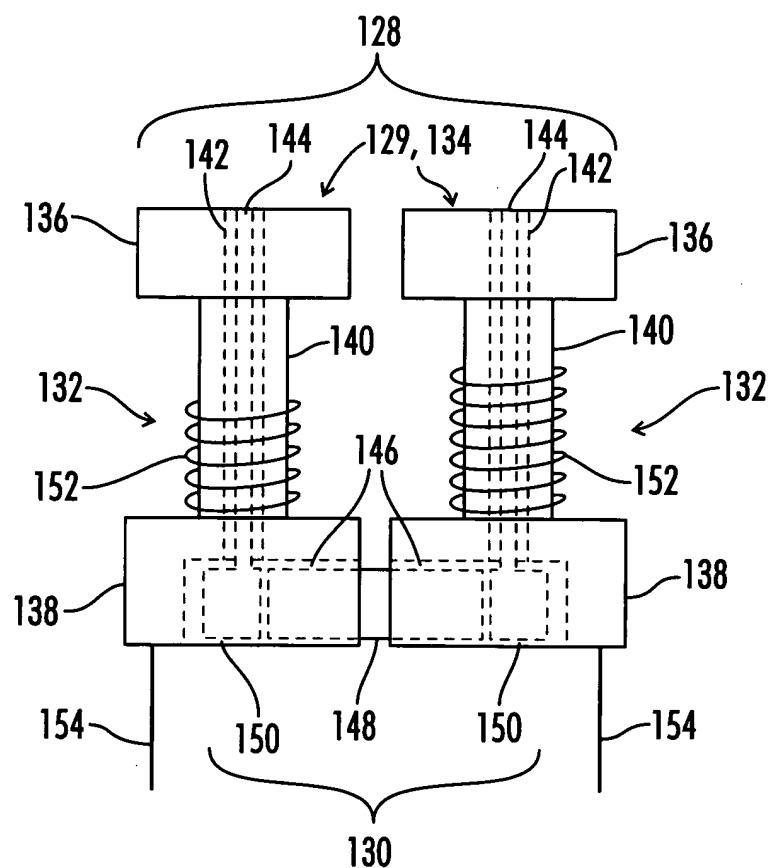


FIG. 8

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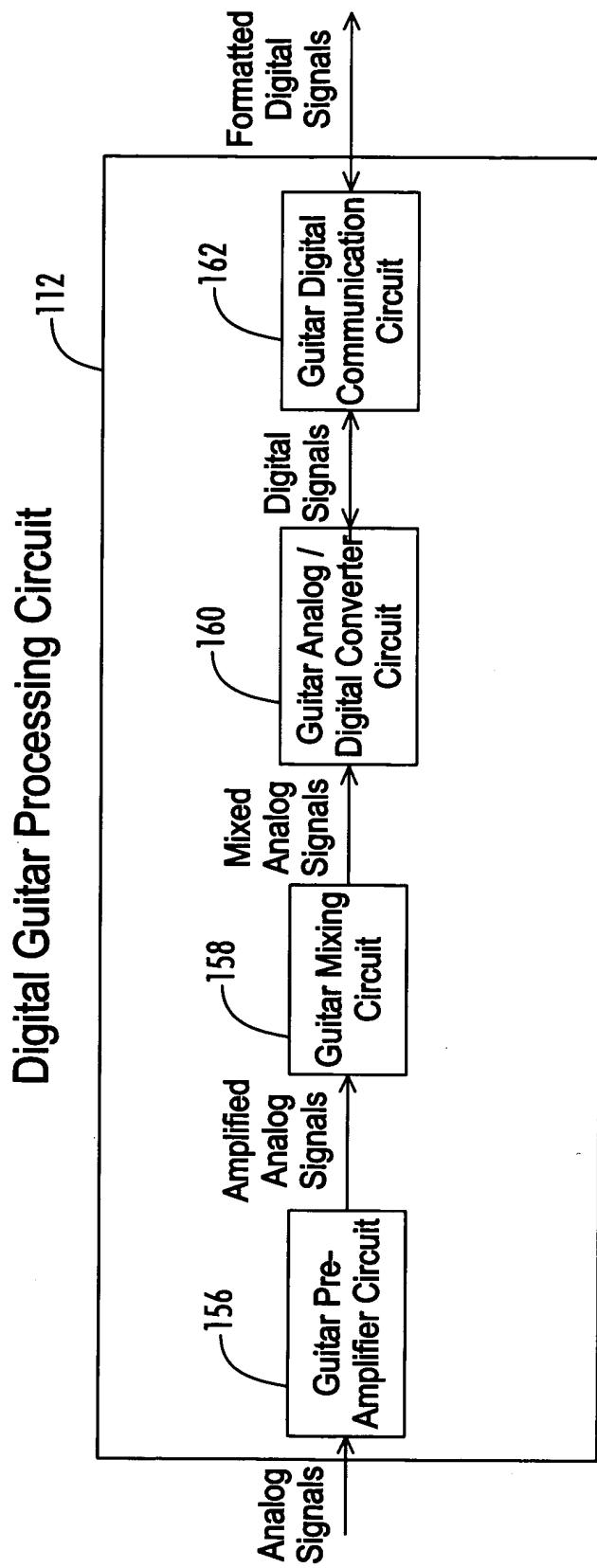


FIG. 9

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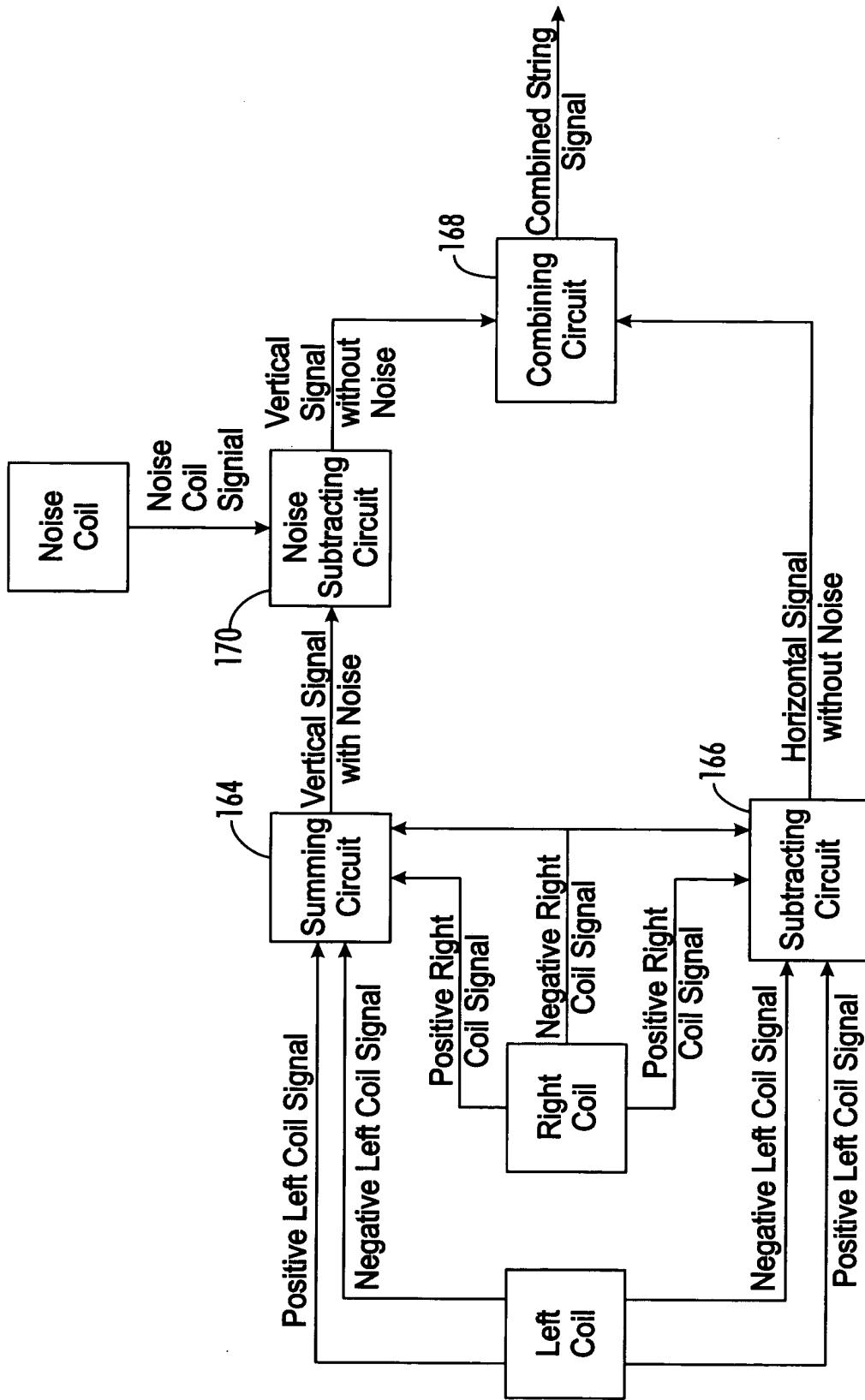


FIG. 10

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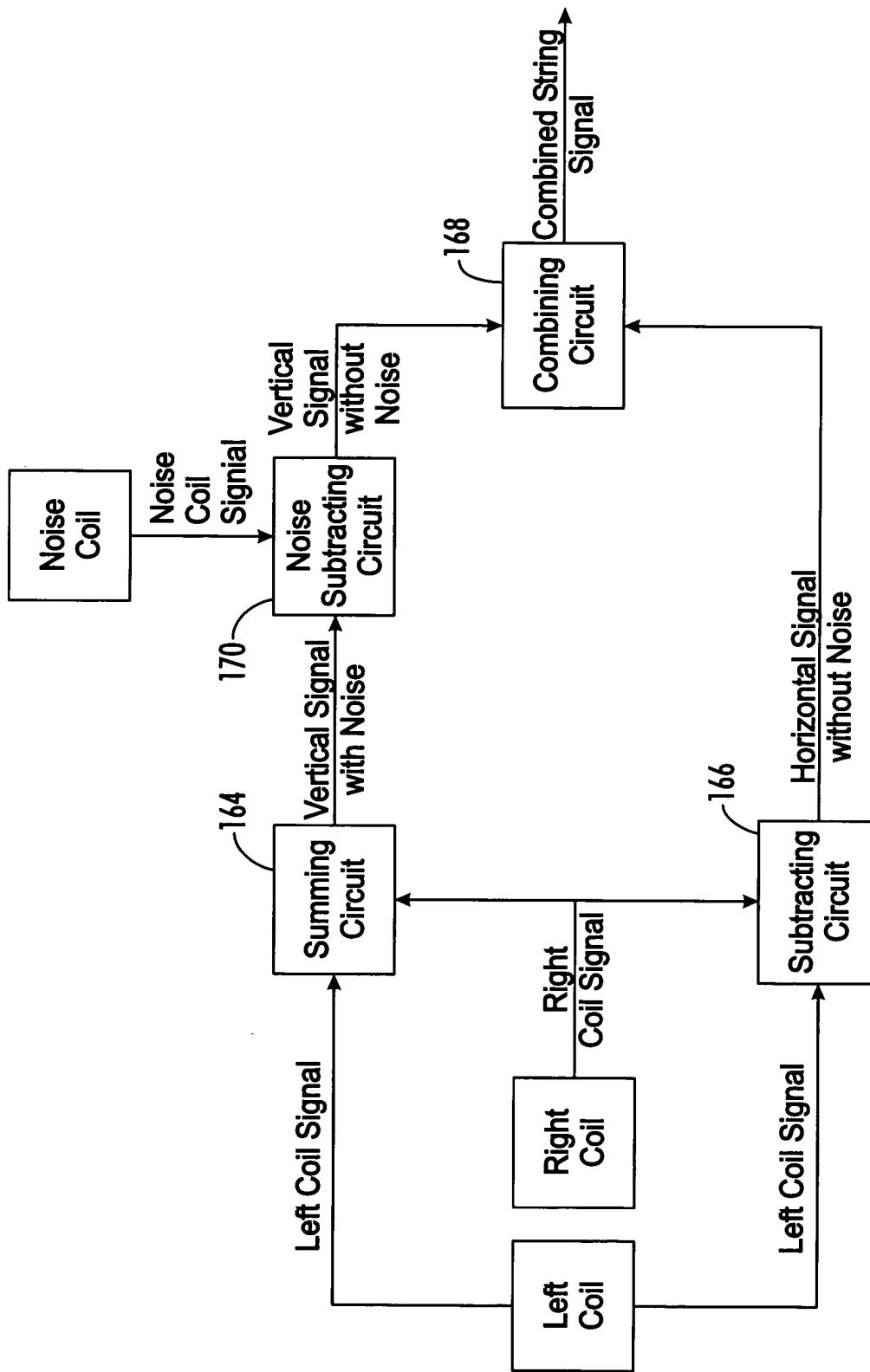


FIG. 11

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Guitar Digital Communication Circuit

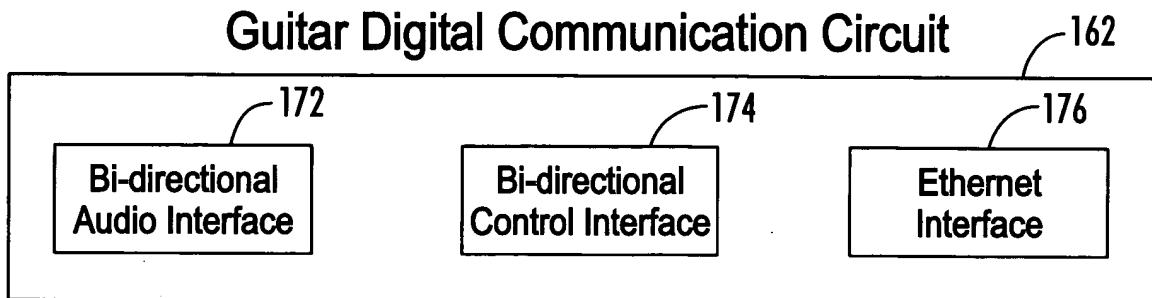


FIG. 12

Guitar Digital Processing Circuit

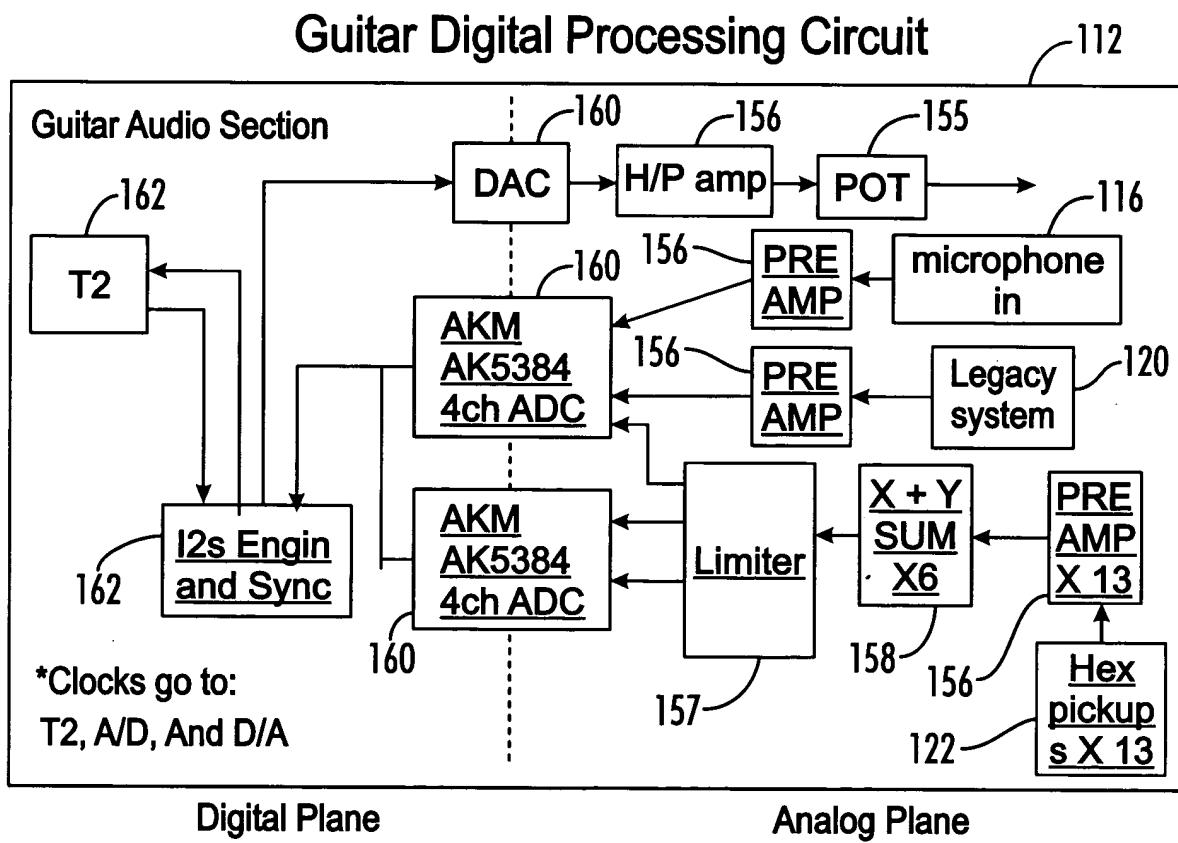


FIG. 13

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Guitar Control Assembly

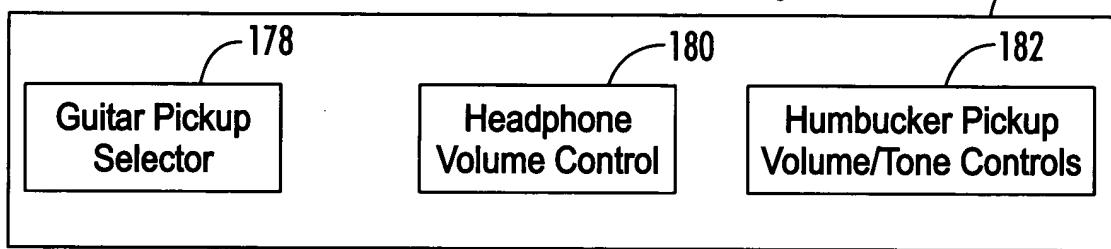


FIG. 14

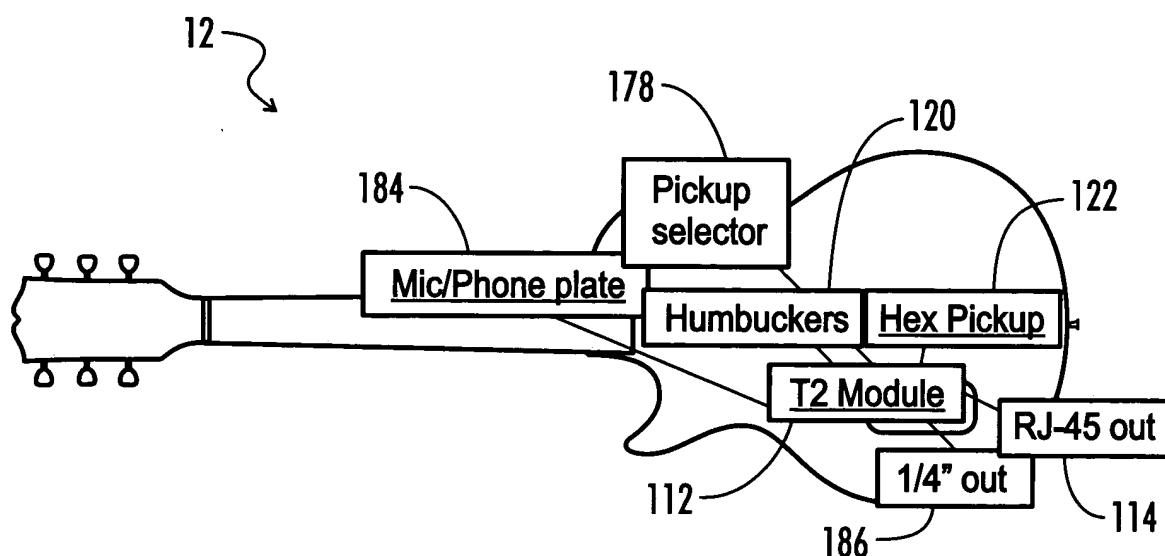
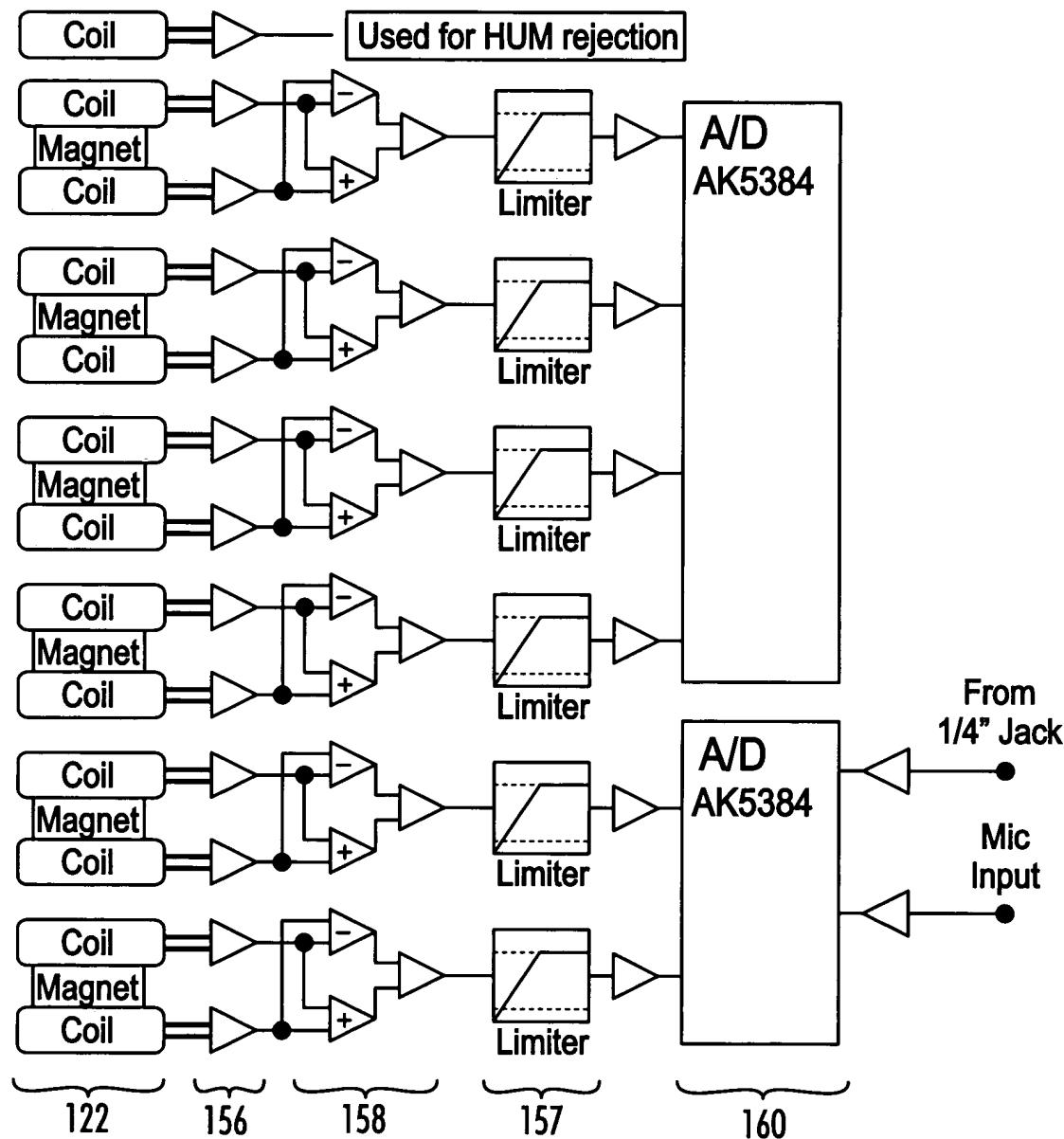


FIG. 15

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Preamplifier Section**FIG. 16**

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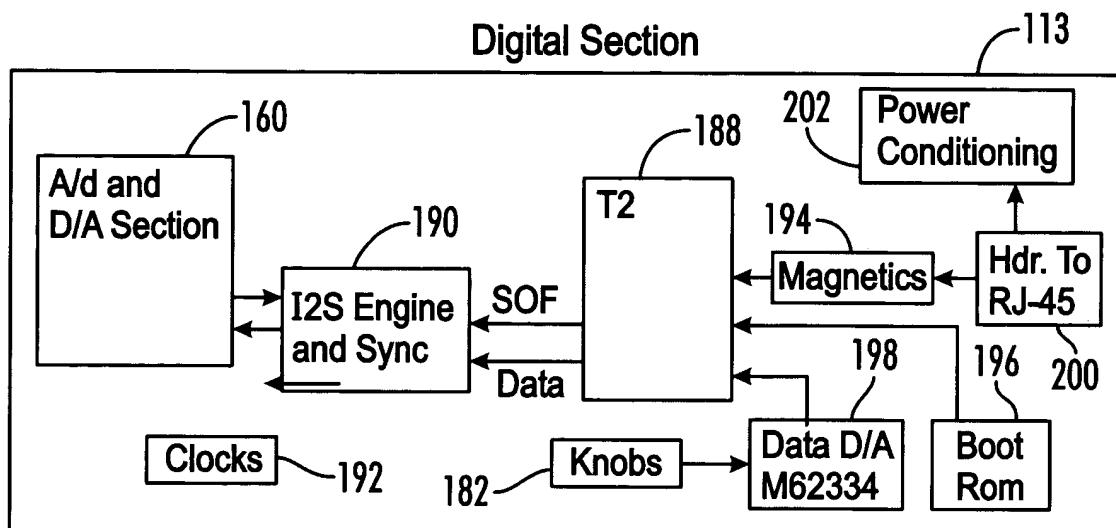


FIG. 17

I2S Engine and Sync

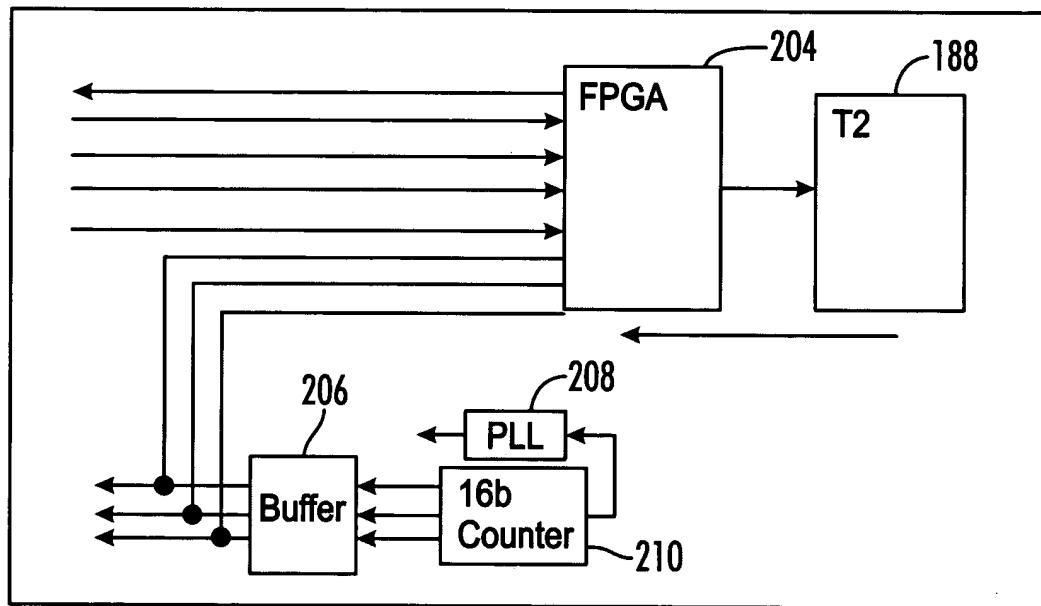


FIG. 18

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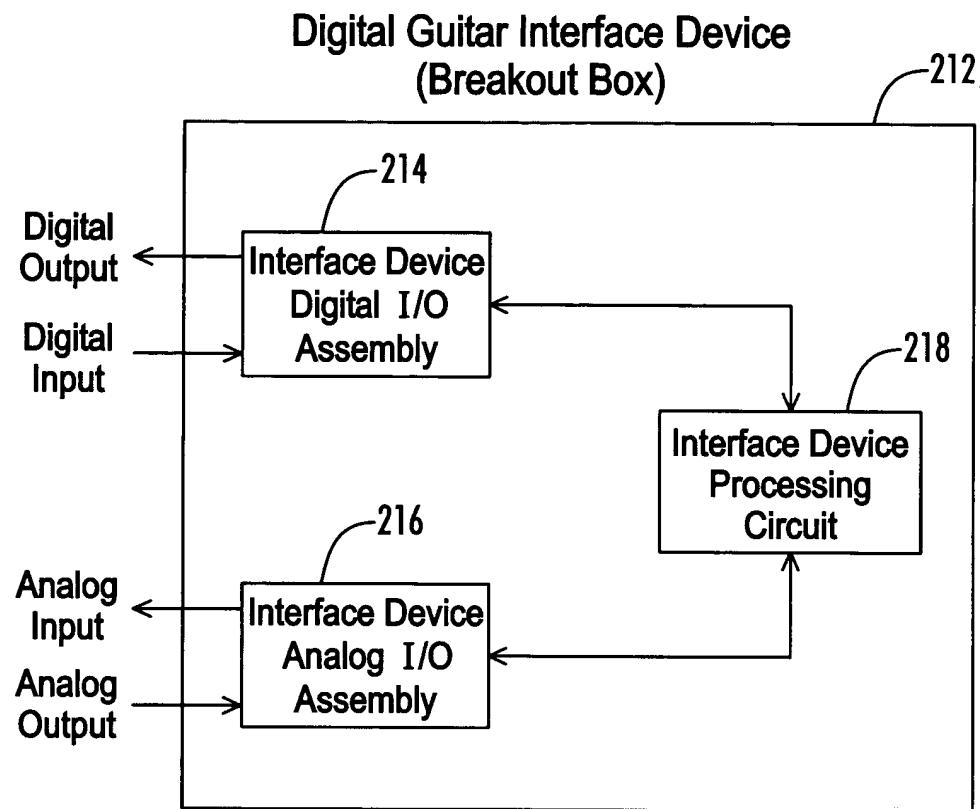


FIG. 19

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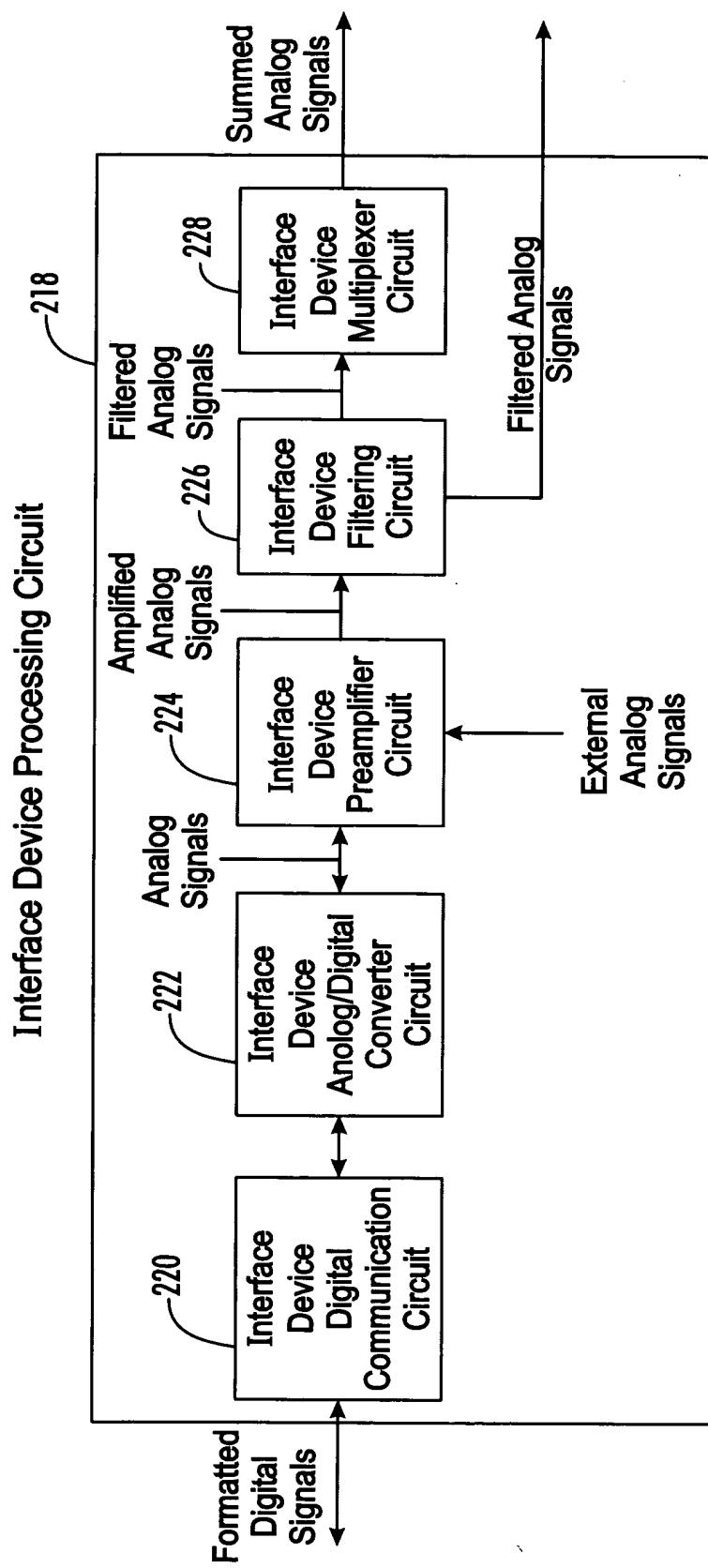


FIG. 20

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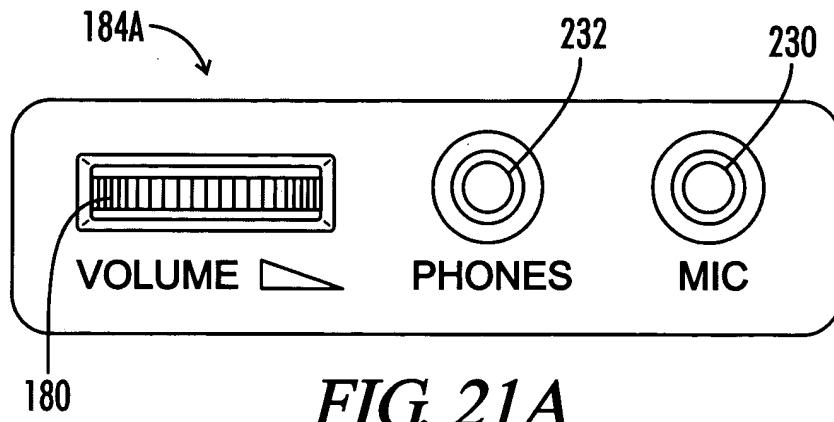


FIG. 21A

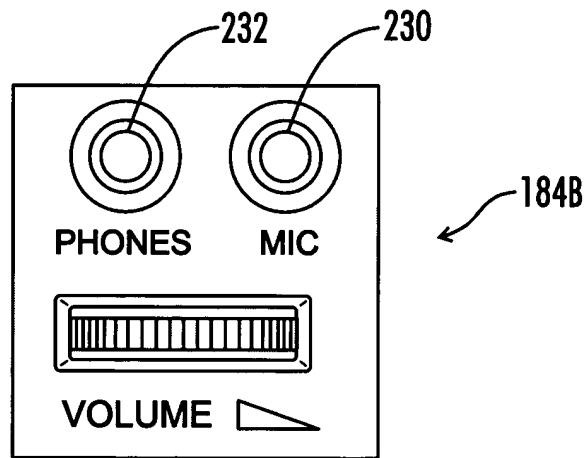


FIG. 21B

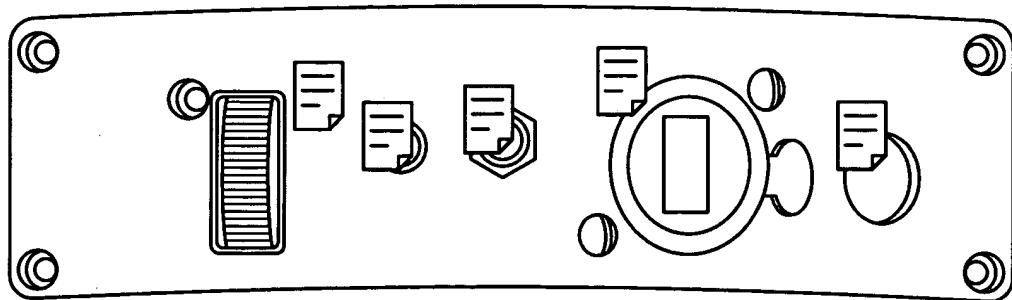


FIG. 21C

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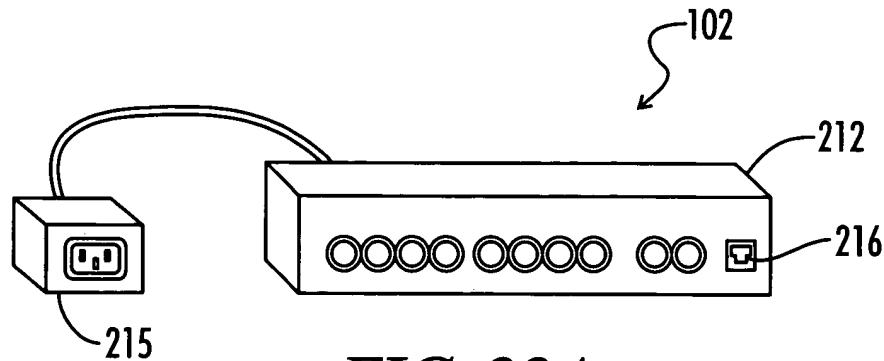


FIG. 22A

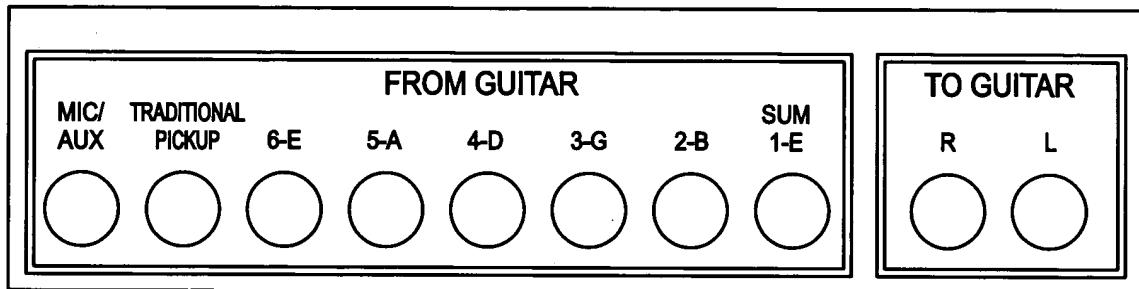


FIG. 22B

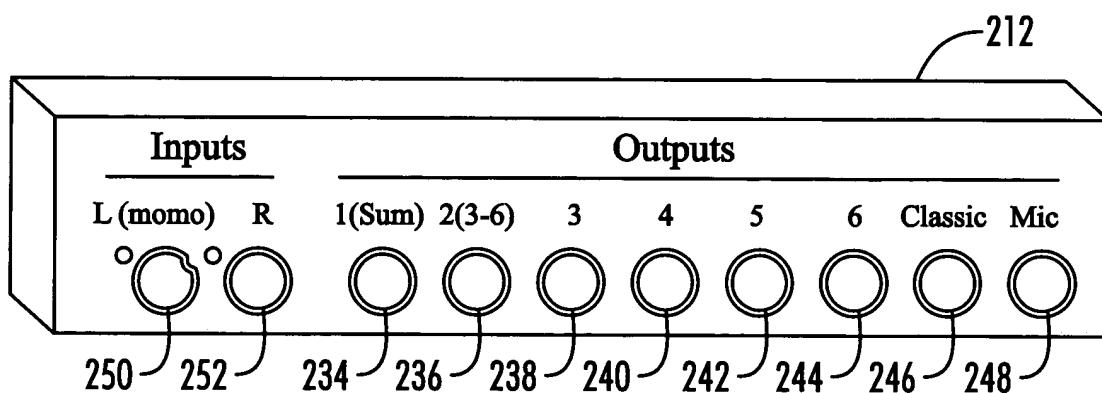


FIG. 23

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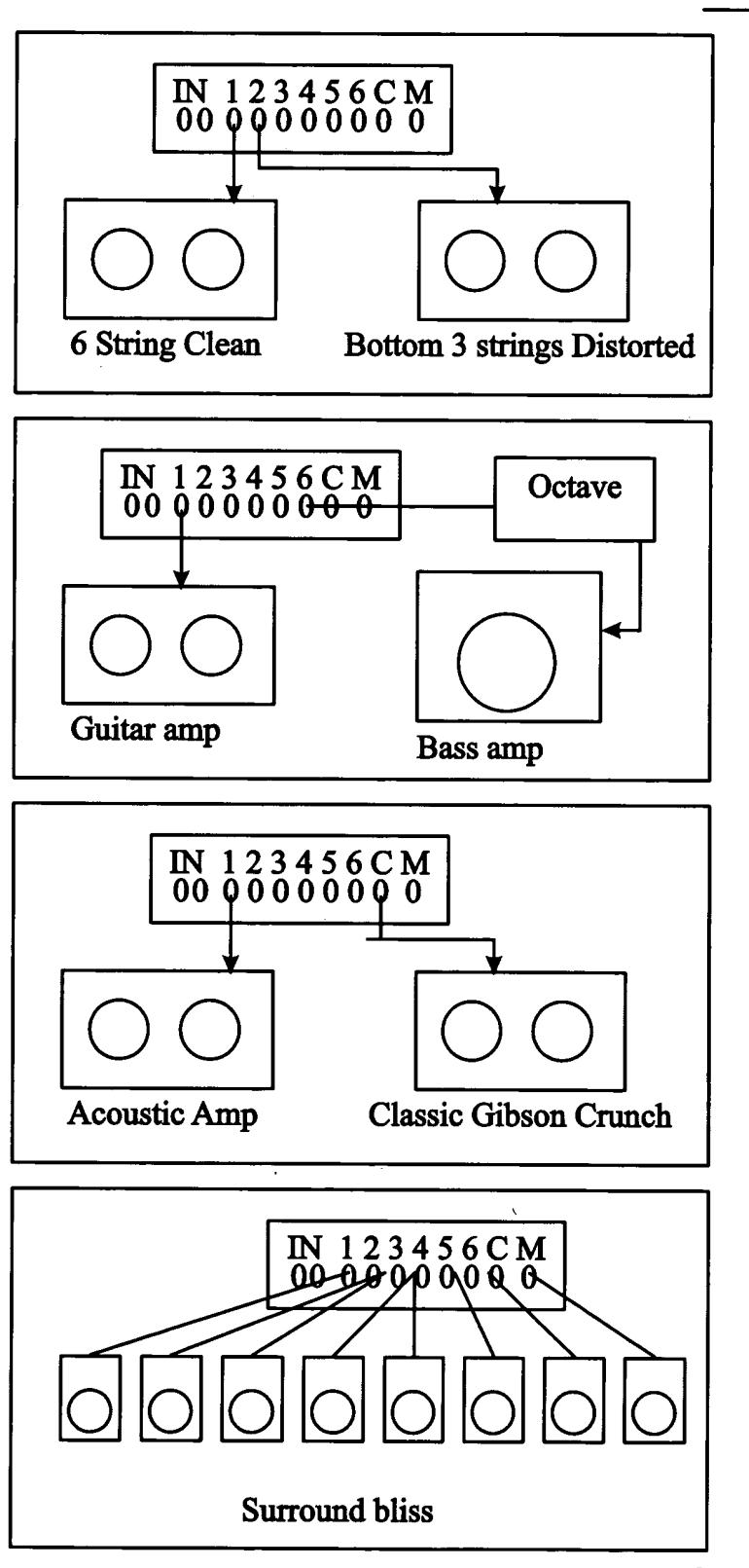


FIG.24

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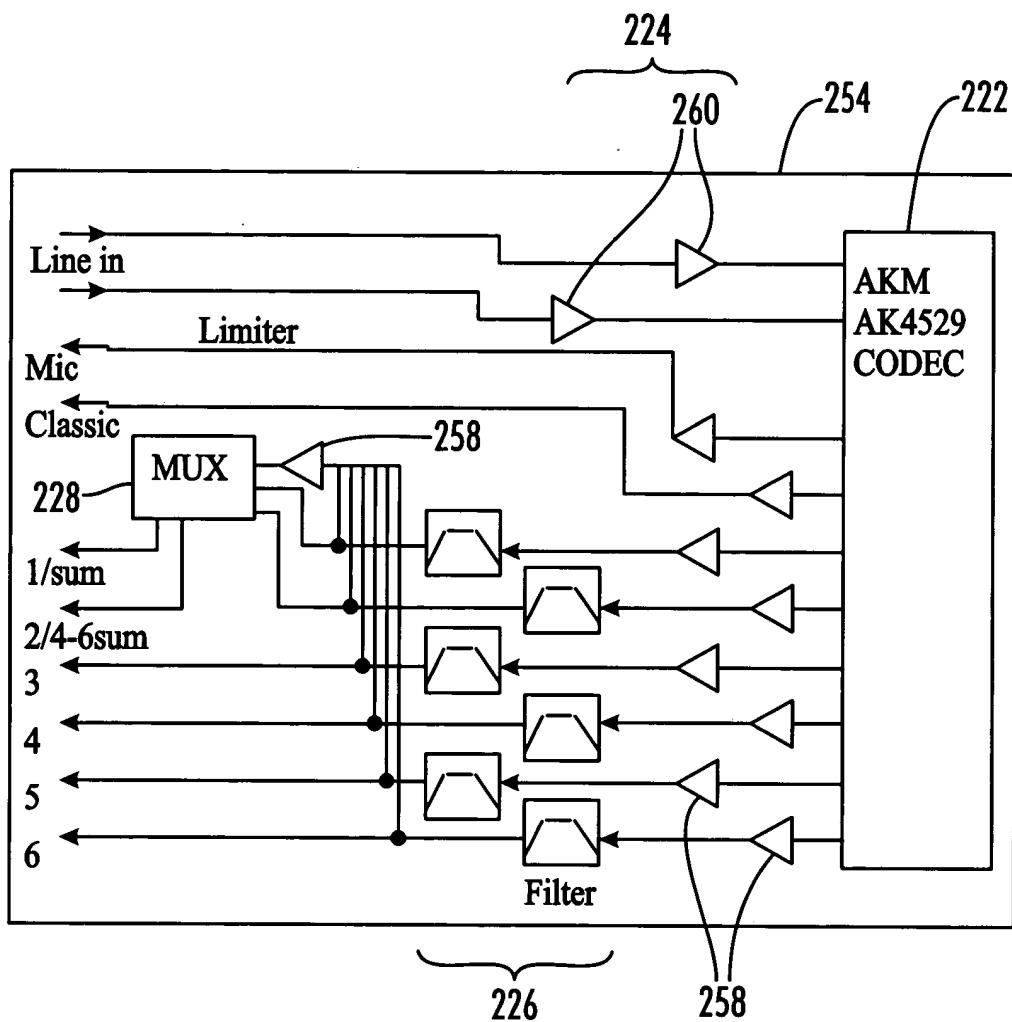


FIG. 25

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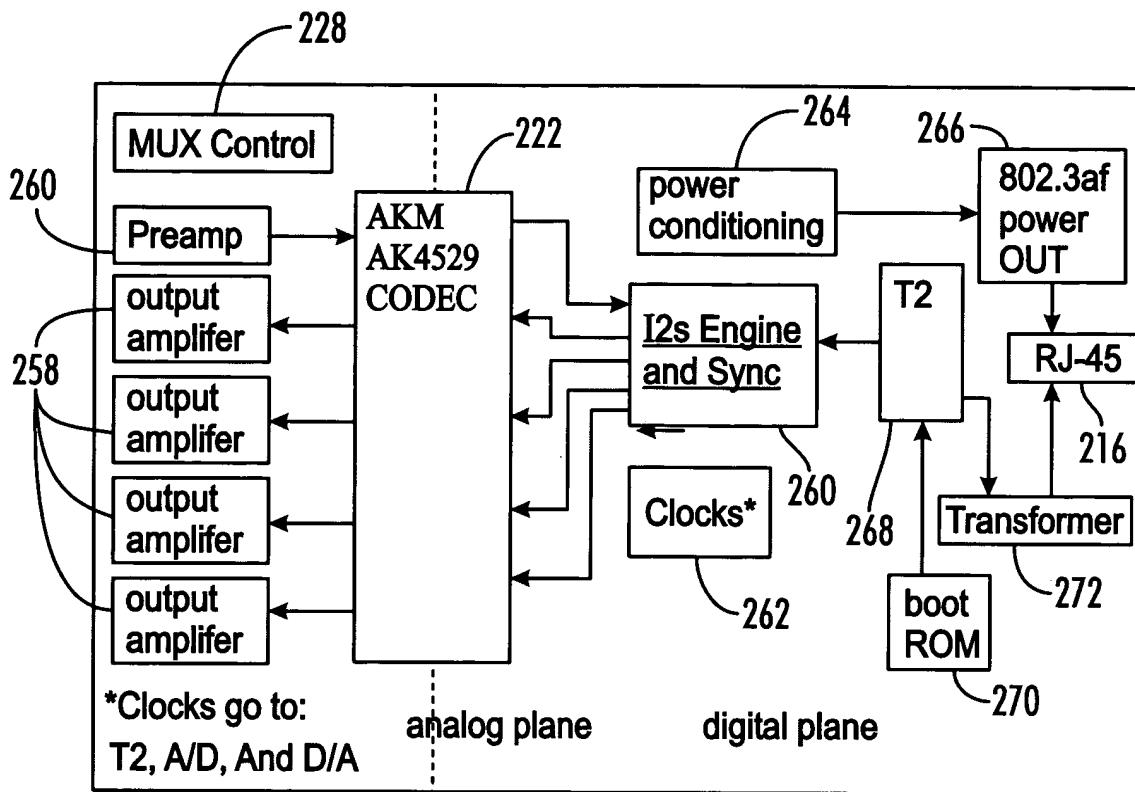


FIG. 26

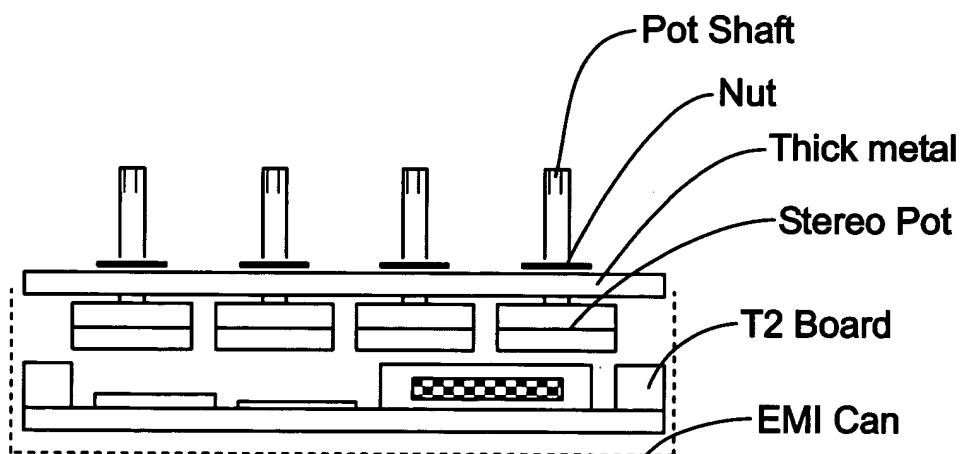


FIG. 27

Passive Legacy system and Control Section

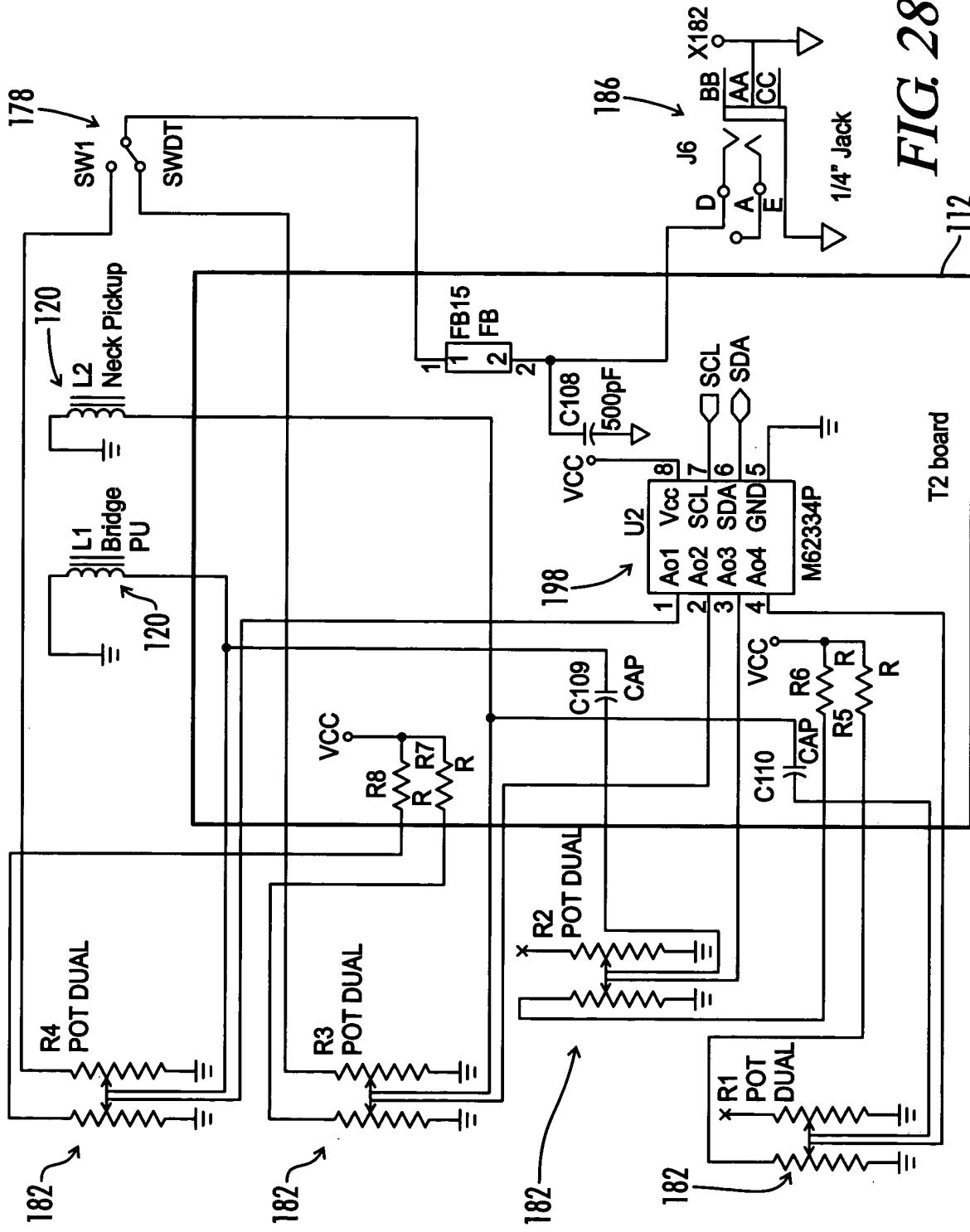


FIG. 28

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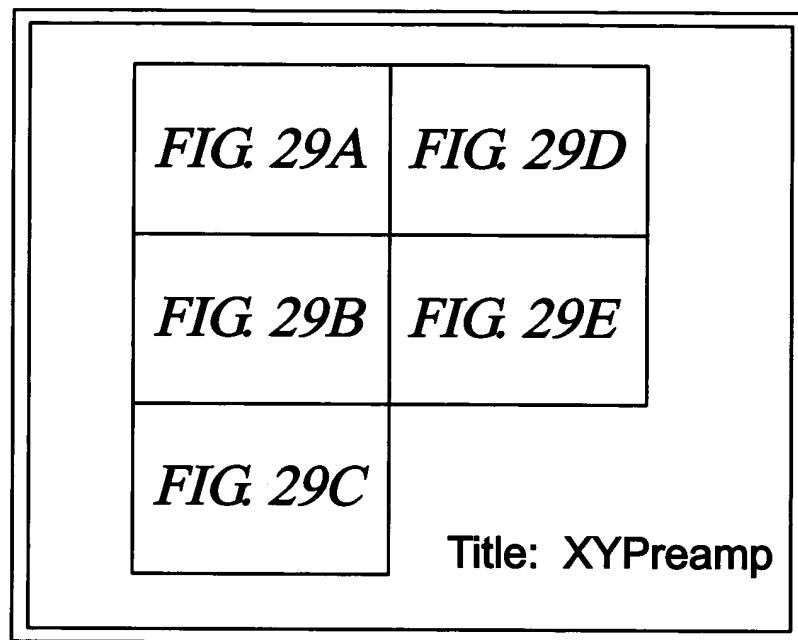
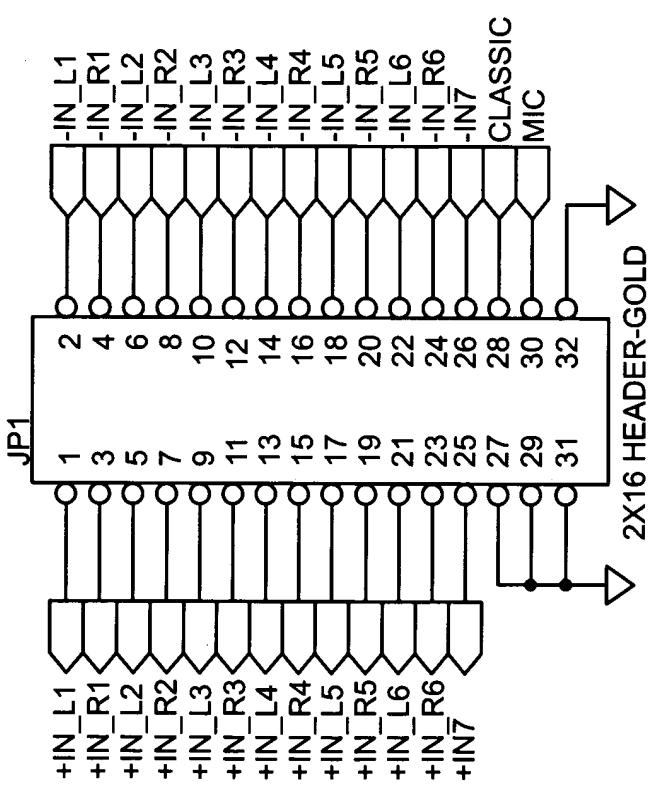


FIG 29

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Pickup & Mic Inputs
Connector to be specified



HEX Pickup

- +IN_L1, -IN_L1, +IN_R1, -IN_R1 = High E string
- +IN_L2, -IN_L2, +IN_R2, -IN_R2 = B string
- +IN_L3, -IN_L3, +IN_R3, -IN_R3 = G string
- +IN_L4, -IN_L4, +IN_R4, -IN_R4 = D string
- +IN_L5, -IN_L5, +IN_R5, -IN_R5 = A string
- +IN_L6, -IN_L6, +IN_R6, -IN_R6 = Low E string
- +IN_7, -IN_7 = Hum cancellation coil

CLASSIC = Hum Bucker pickup (Classic is actually input from control board. The header connection will be an output to the 1/4" jack)

MIC = microphone input (this input has 12V phantom power for capacitor microphones)

FIG. 29A

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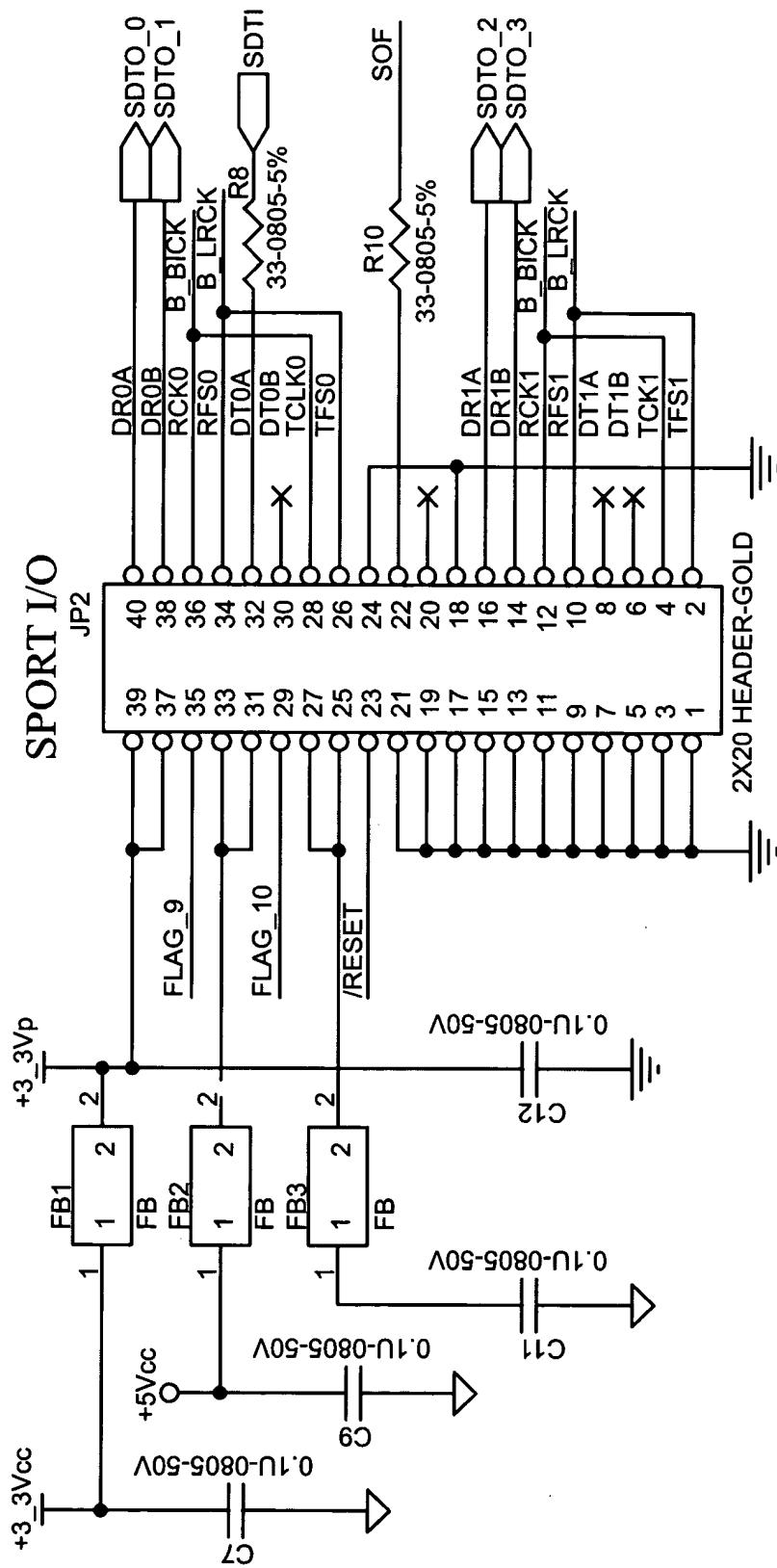


FIG 29B

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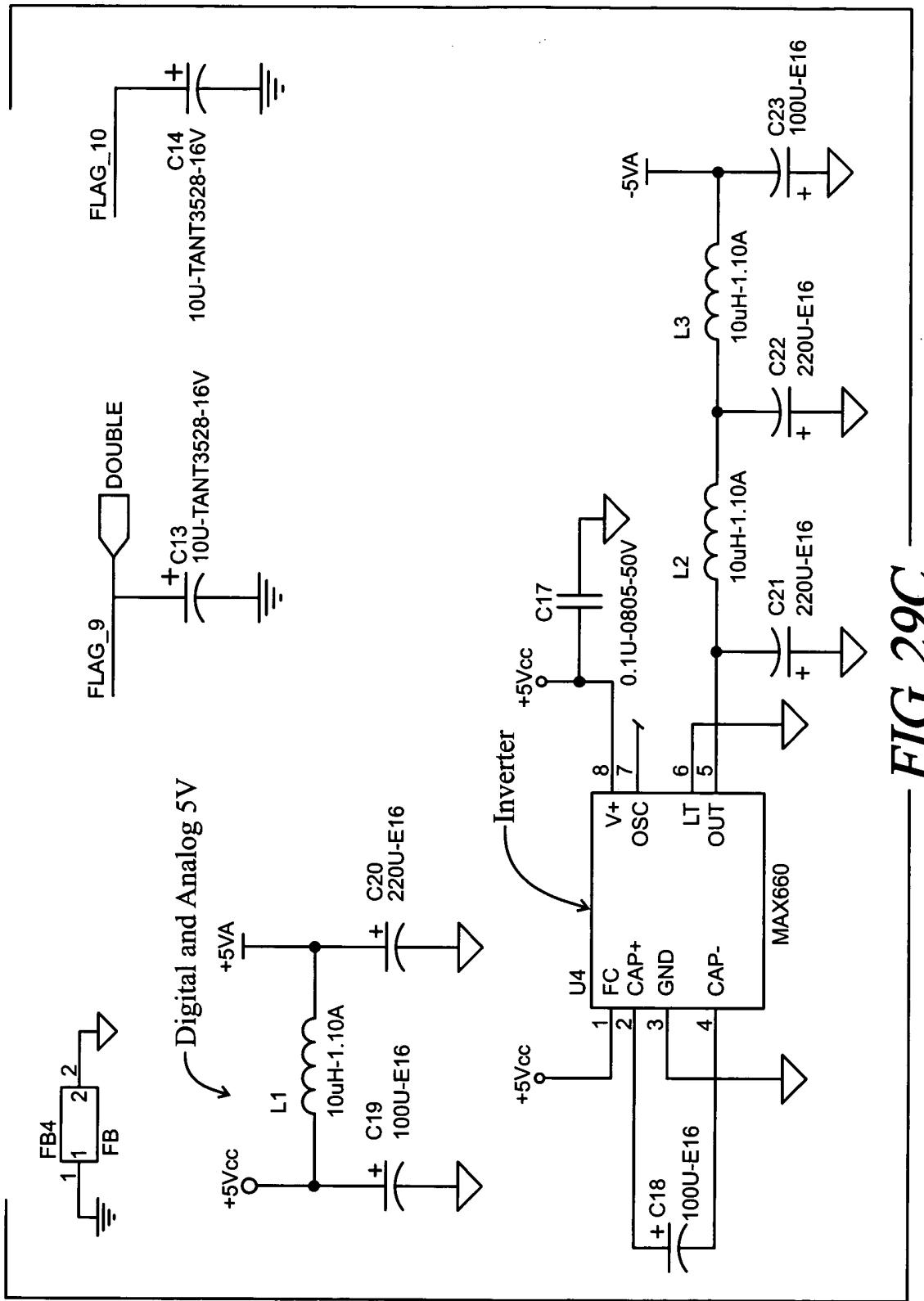


FIG. 29C

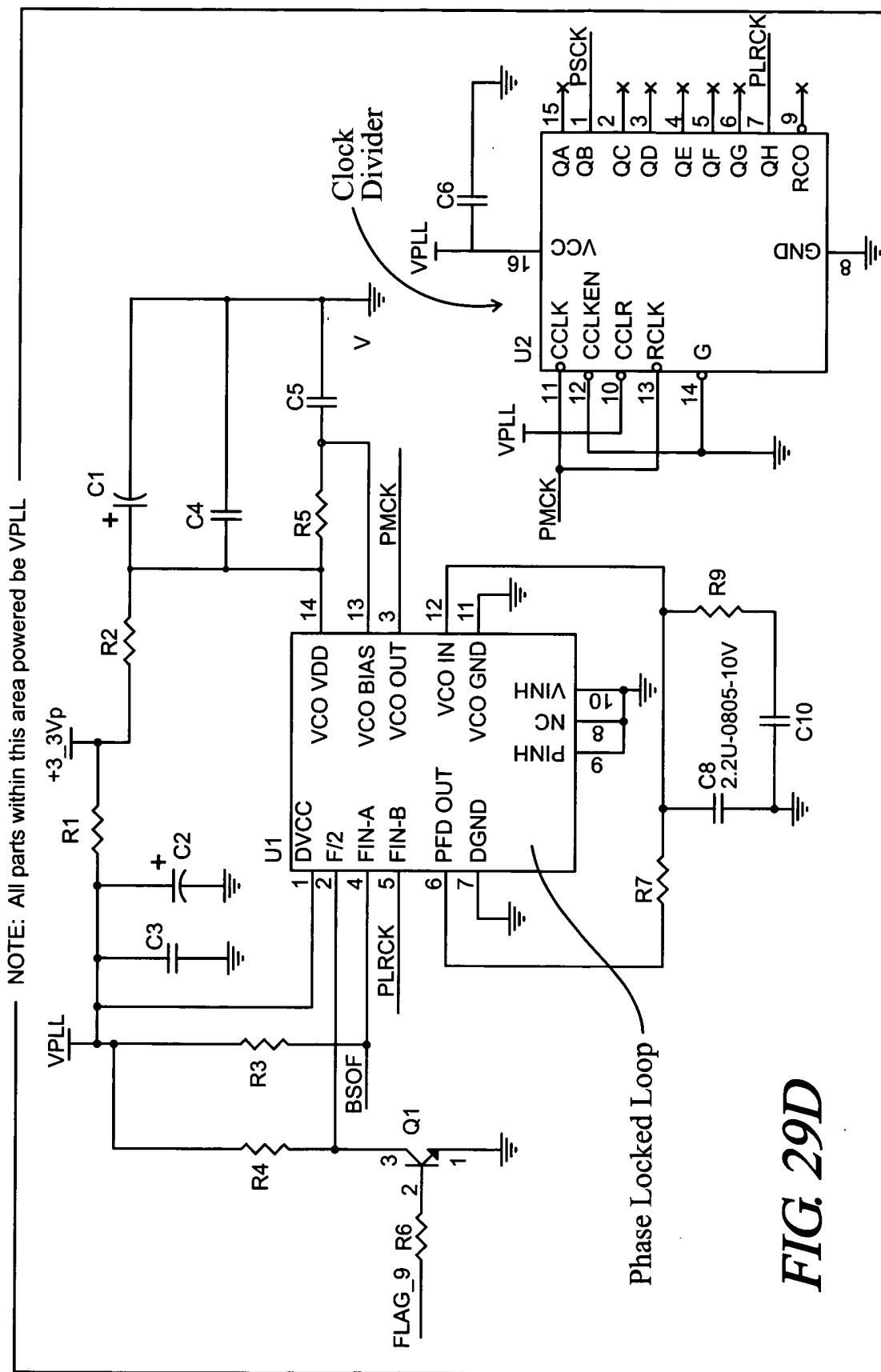


FIG. 29D

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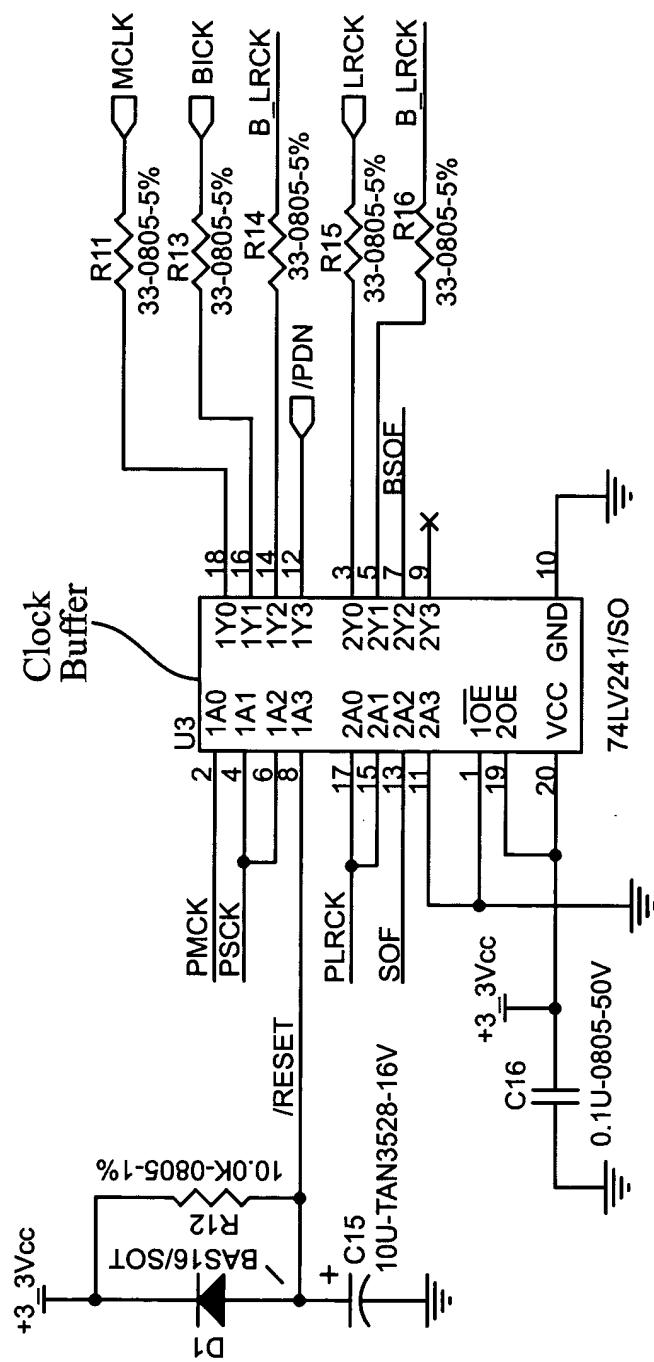


FIG. 29E

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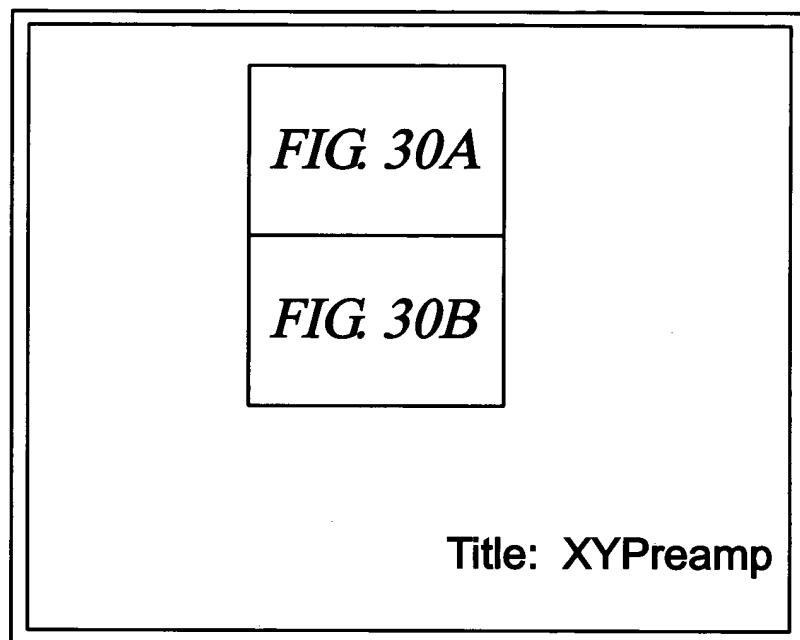
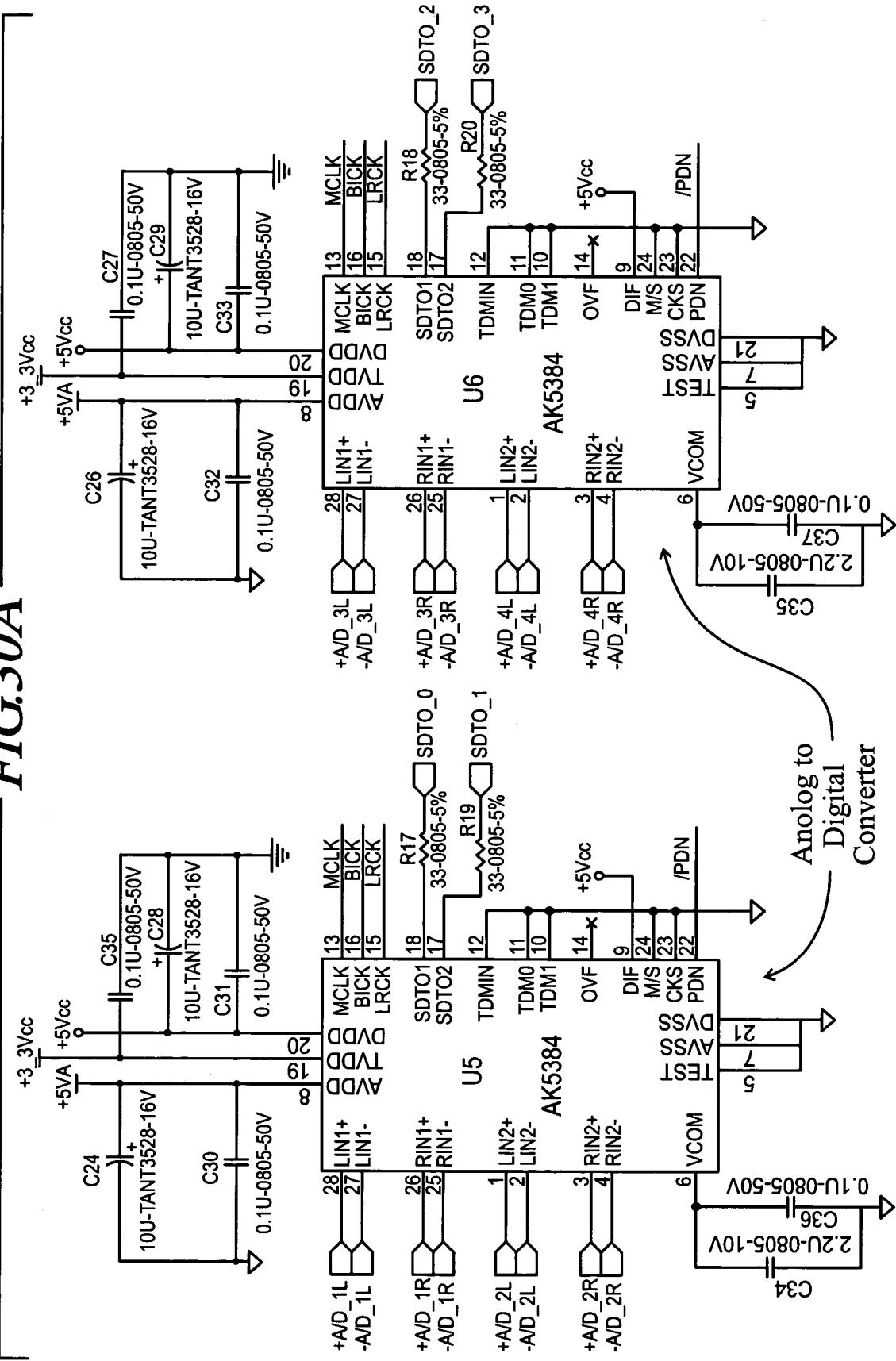


FIG. 30

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FIG.30A



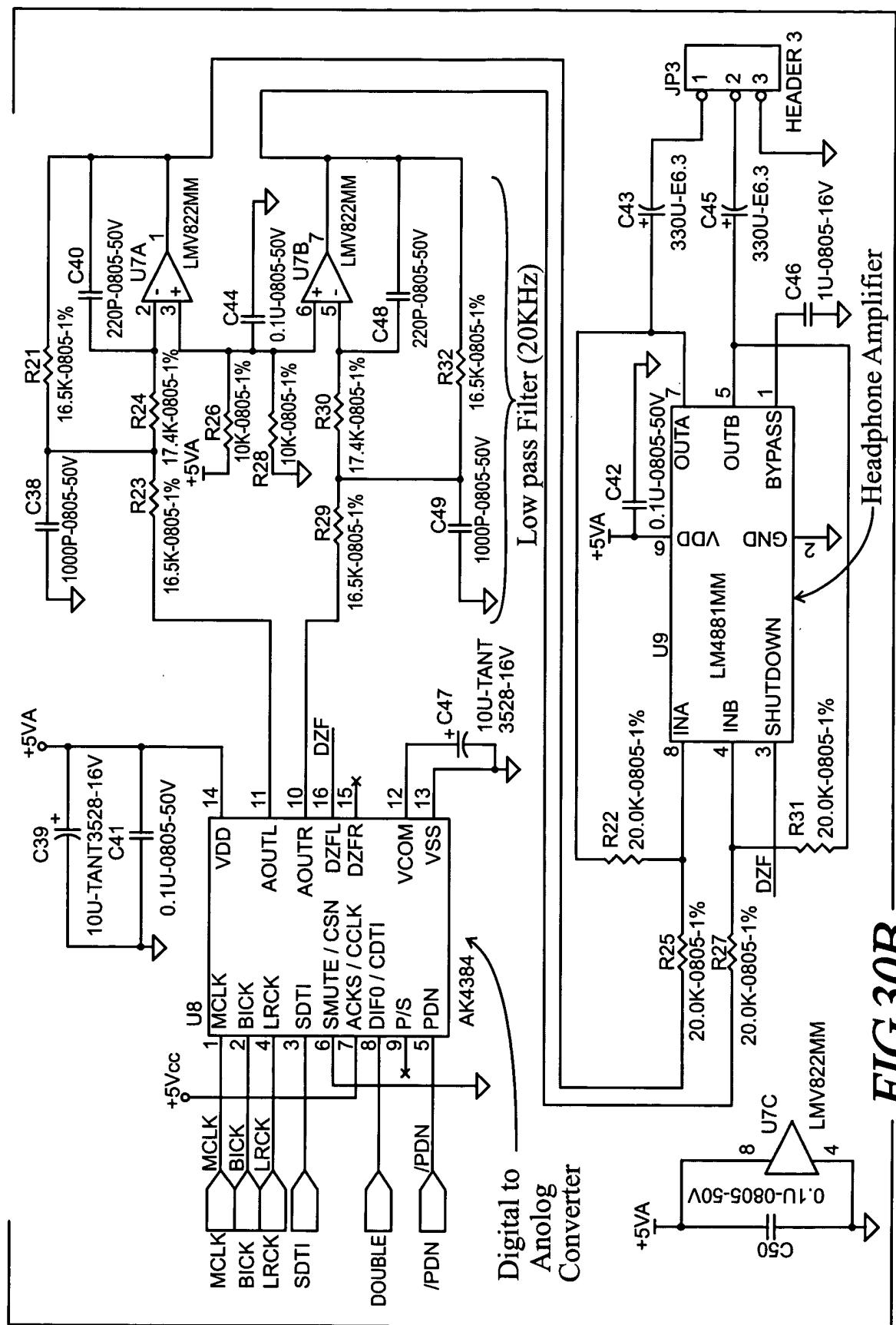


FIG. 30B

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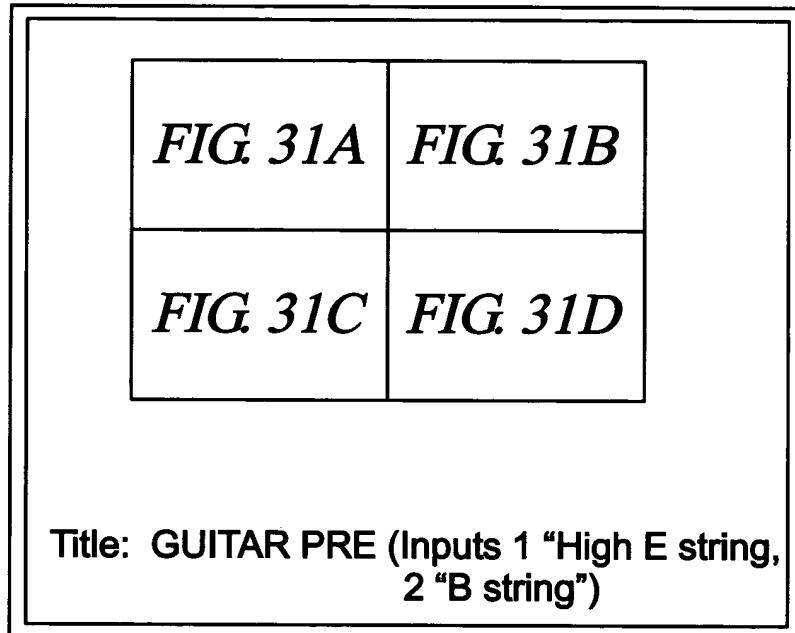


FIG 31

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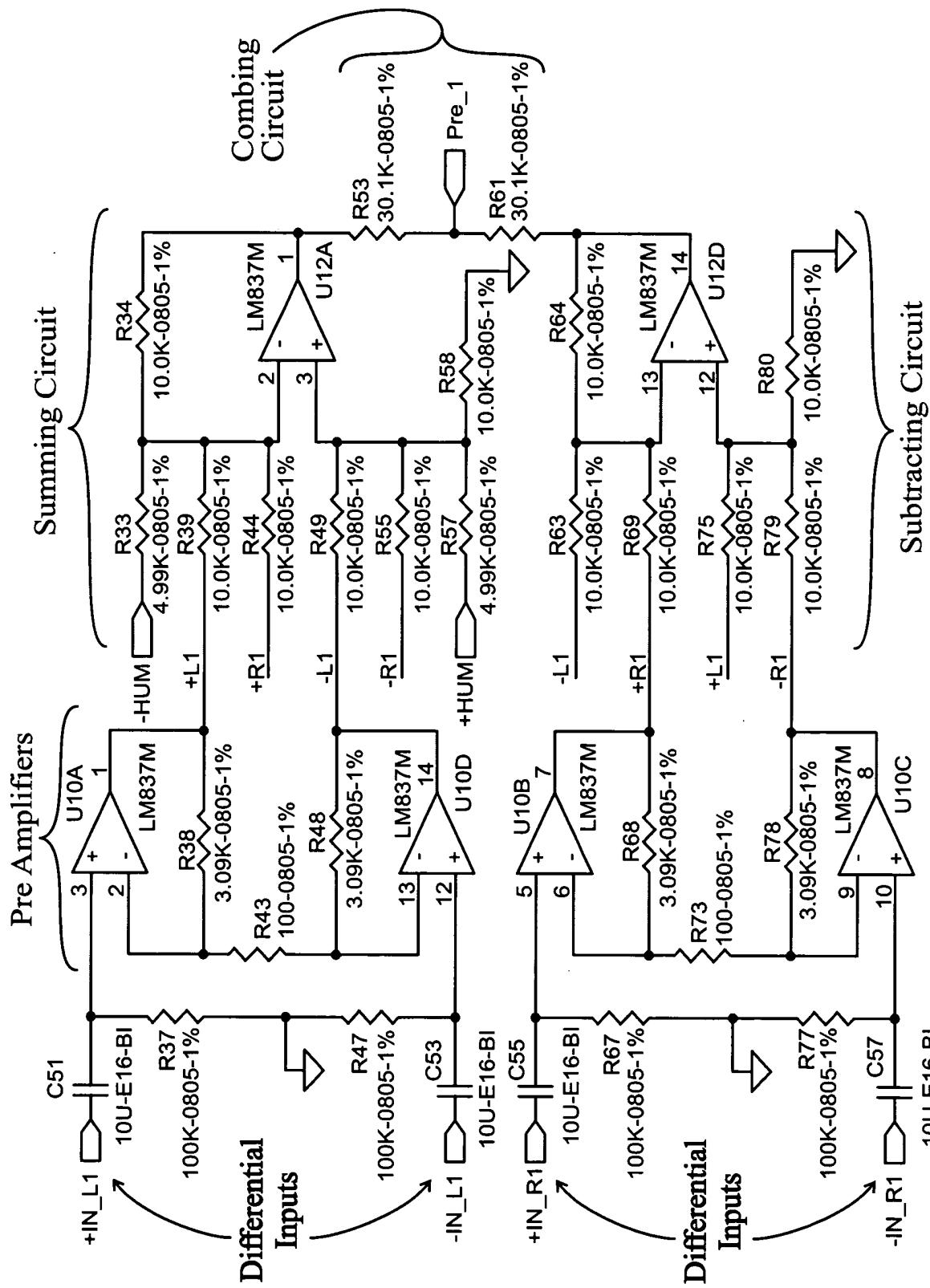
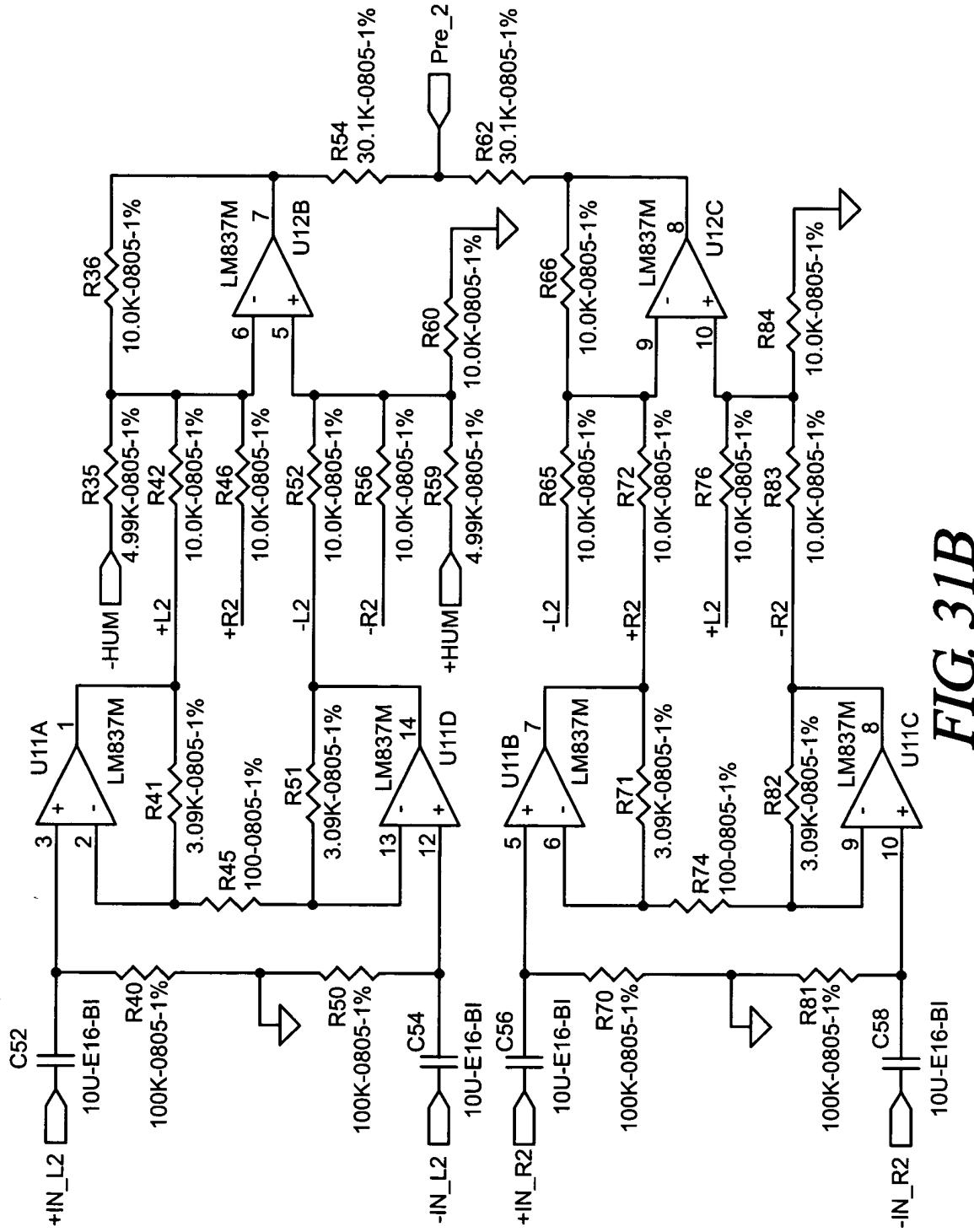
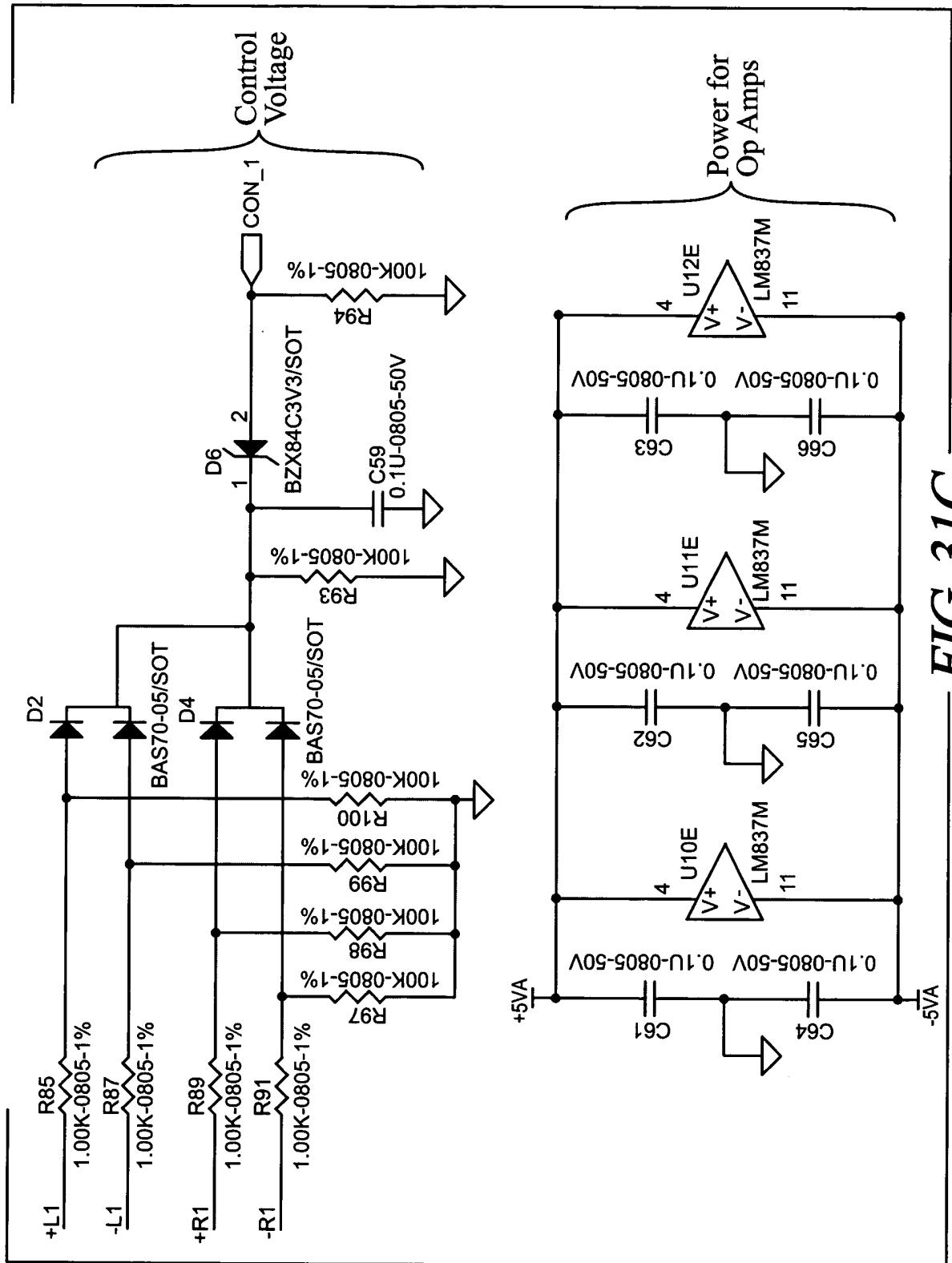


FIG. 31A

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**FIG. 31B**

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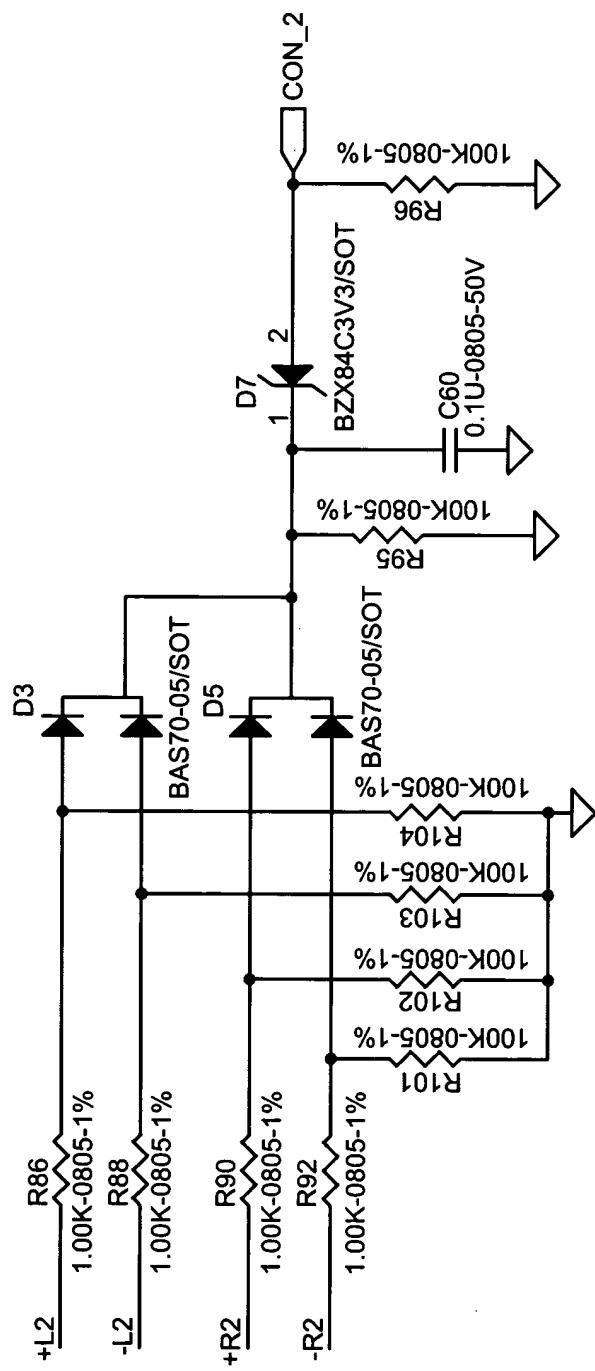


FIG. 31D

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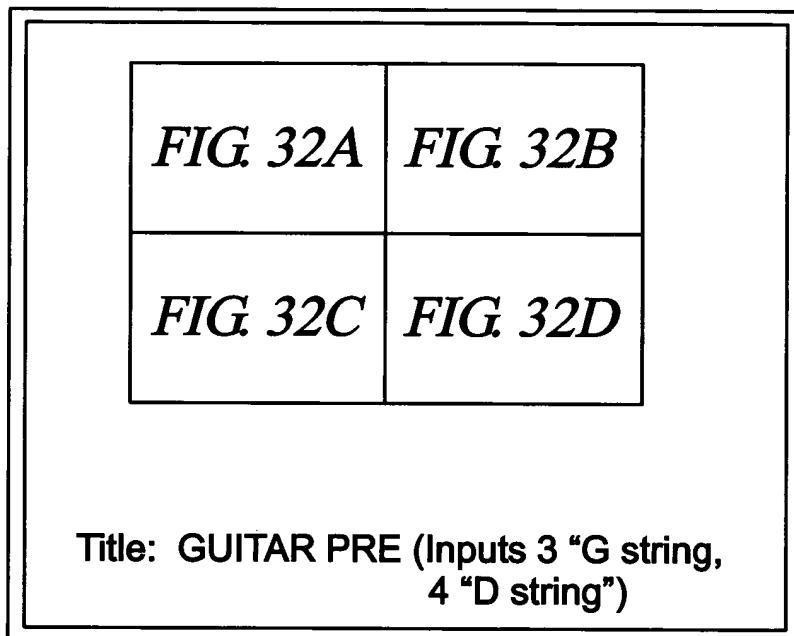


FIG 32

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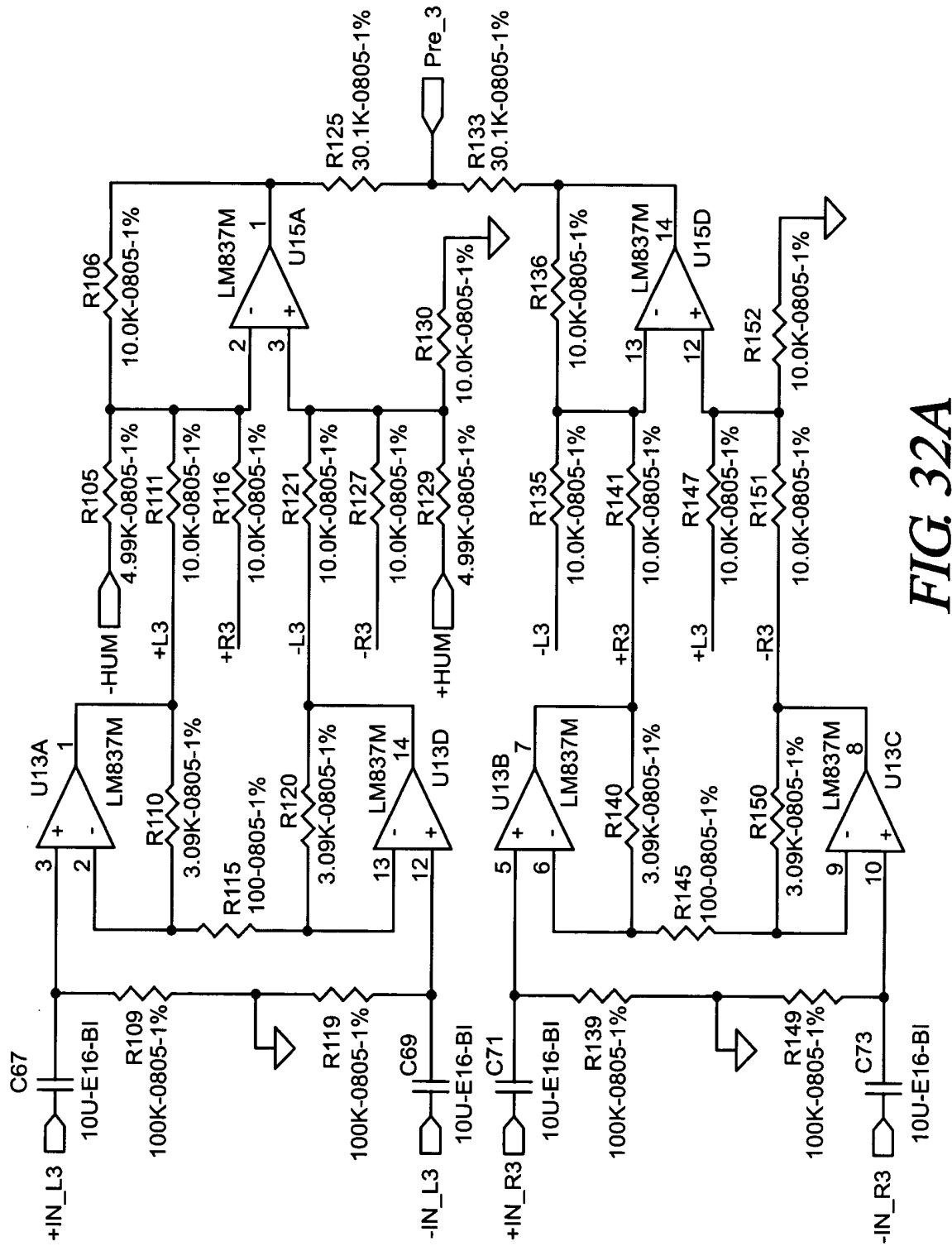


FIG. 32A

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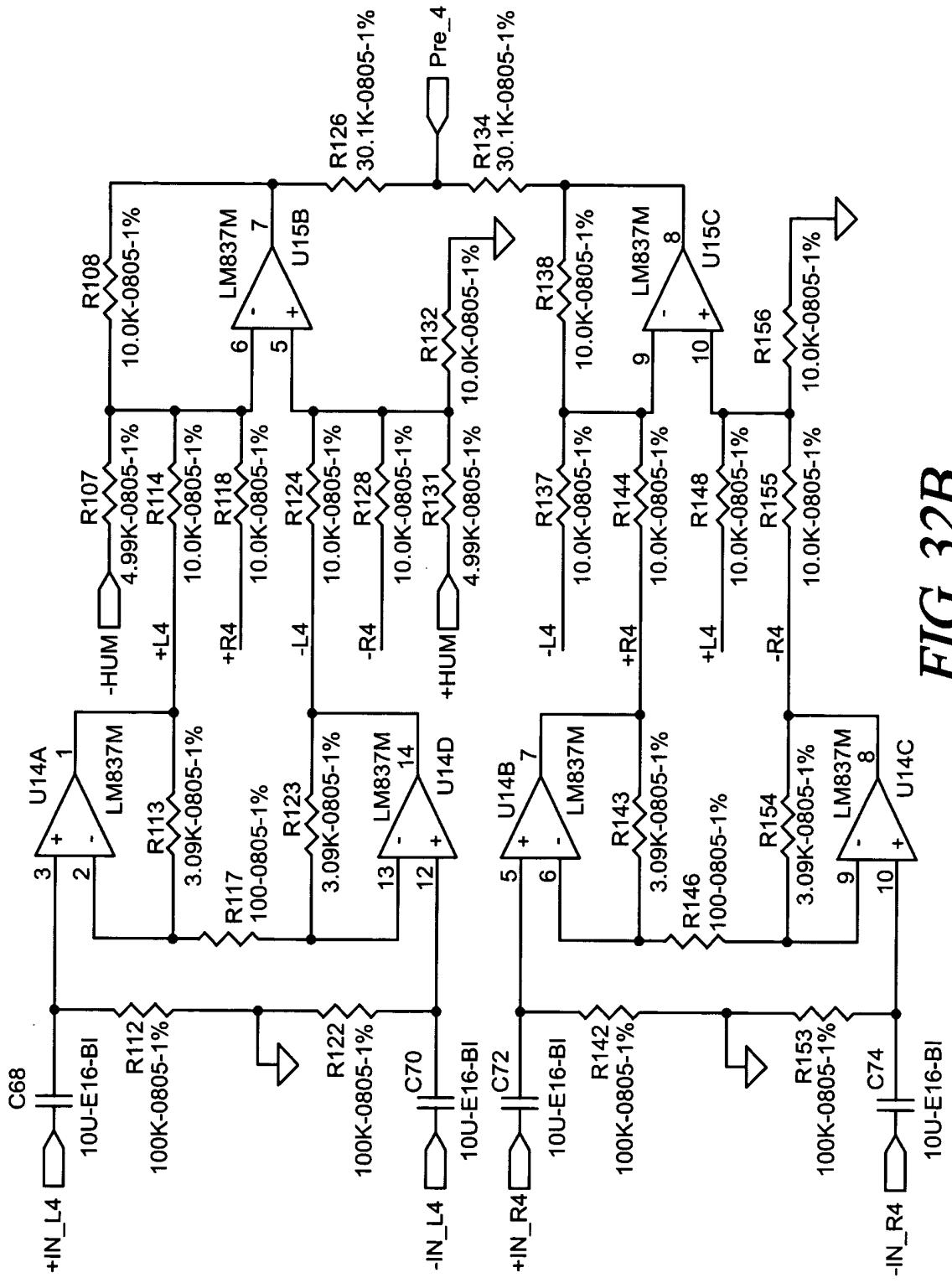
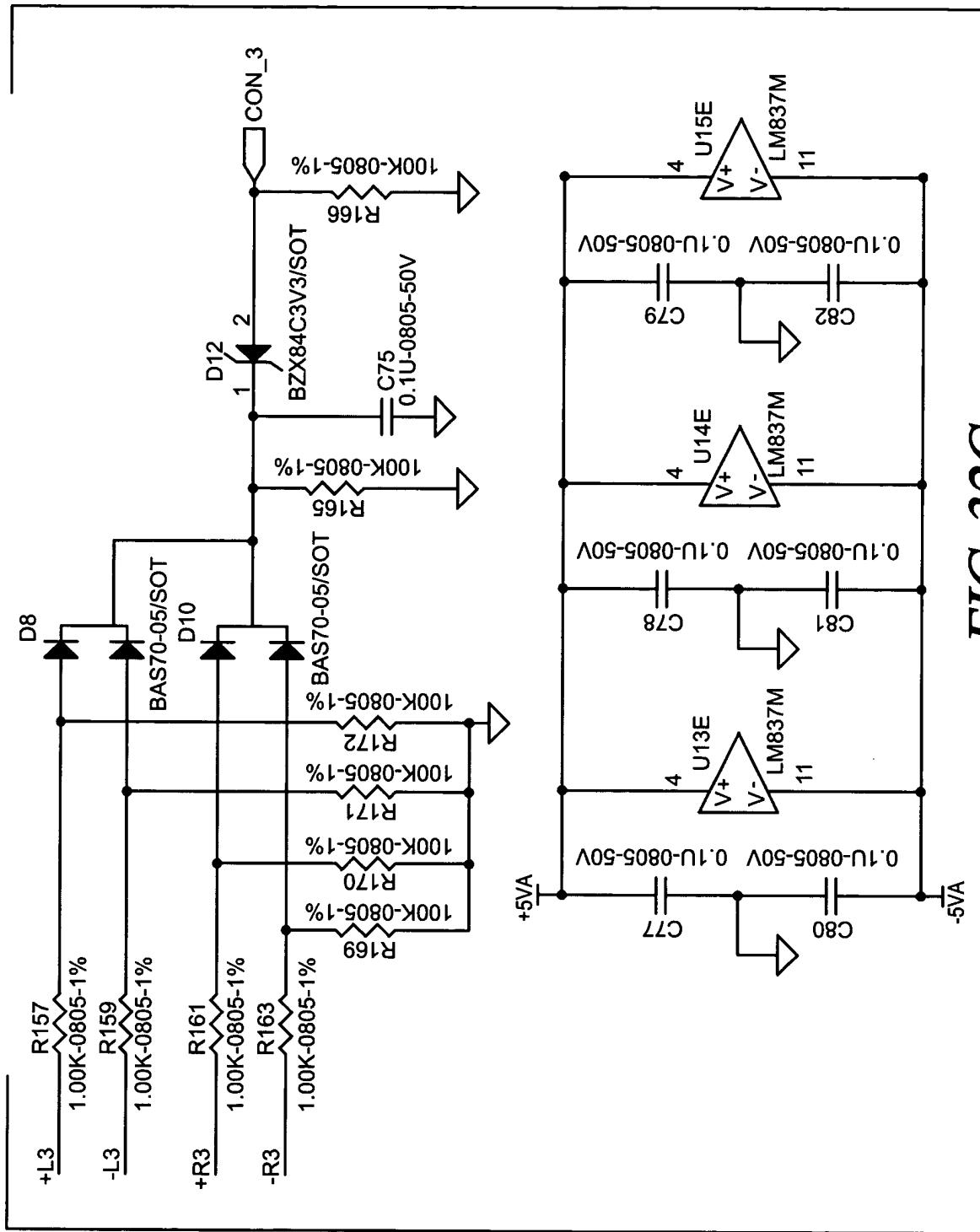


FIG. 32B

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**FIG 32C**

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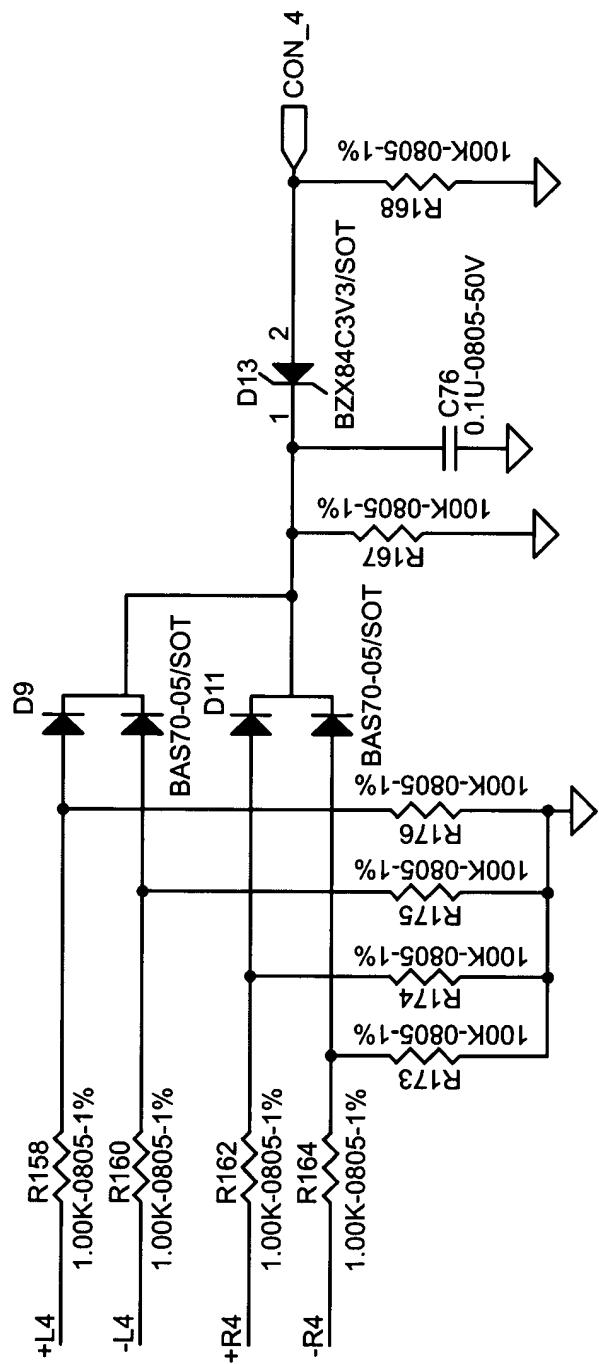


FIG. 32D

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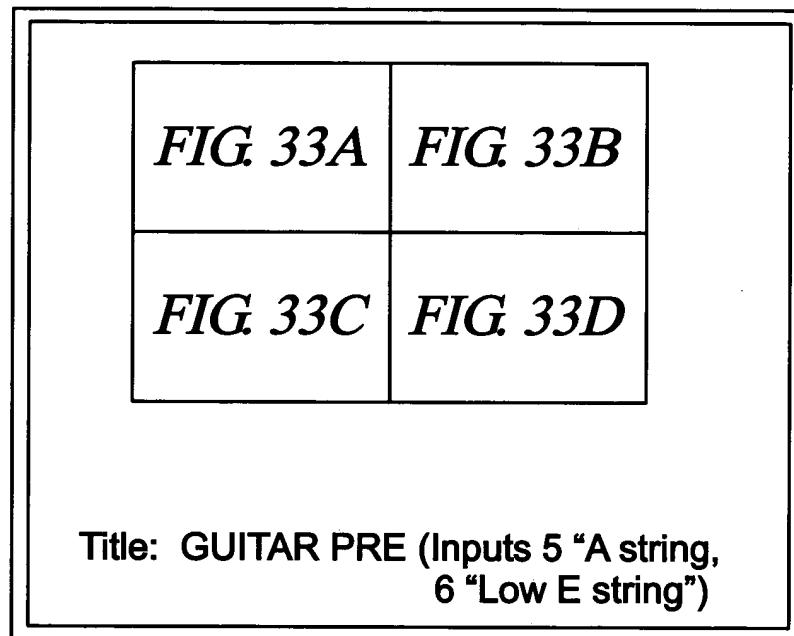


FIG 33

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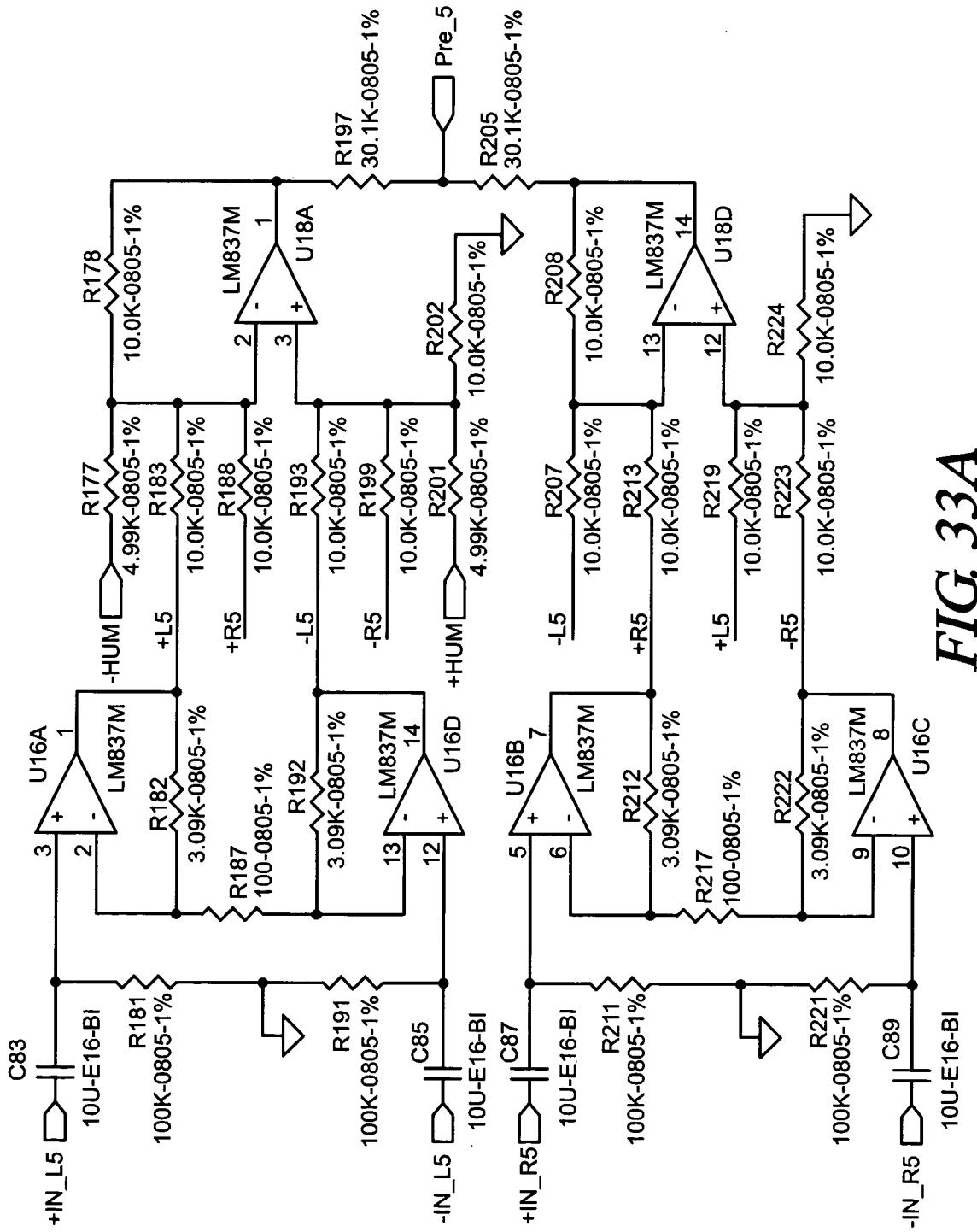
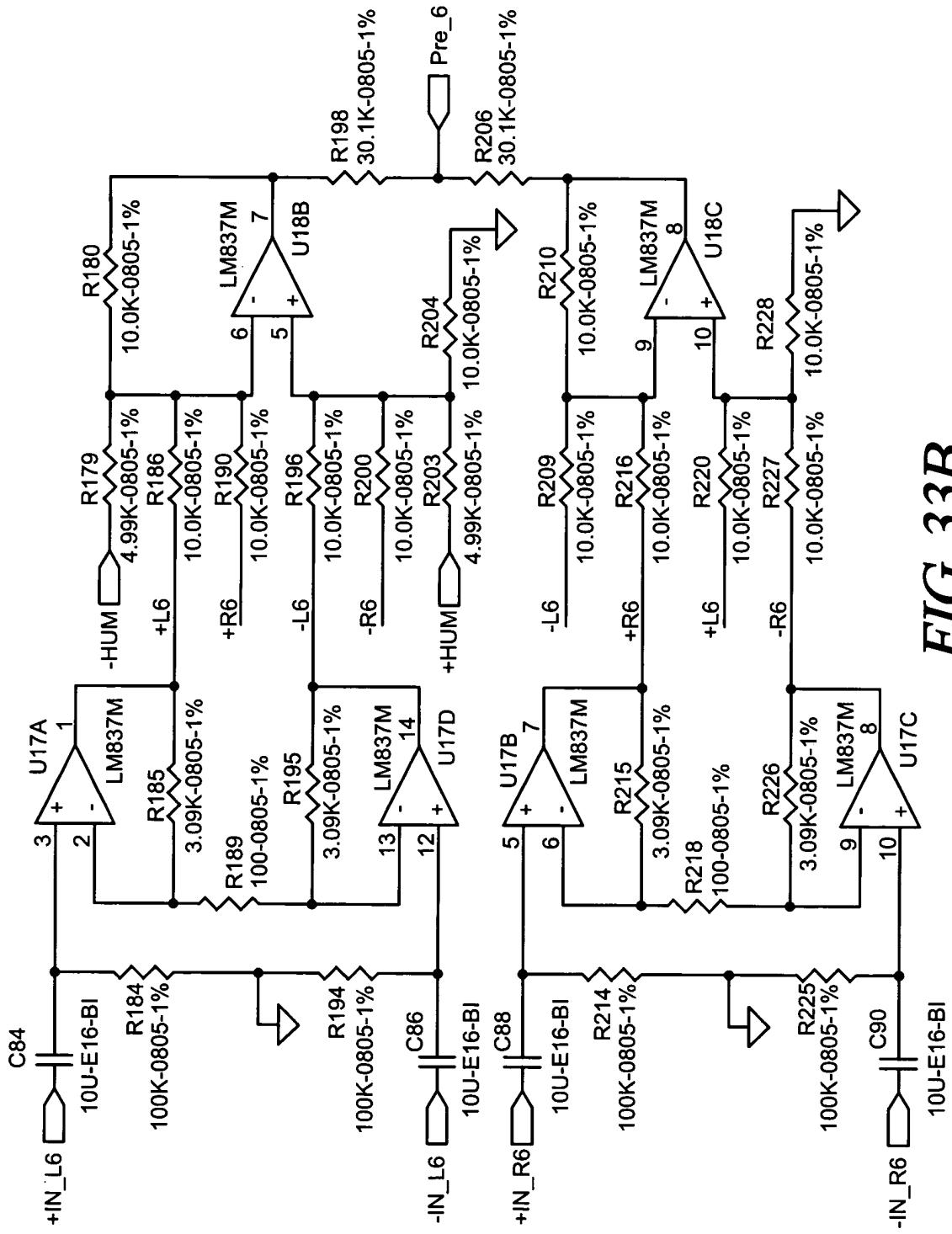


FIG 33A



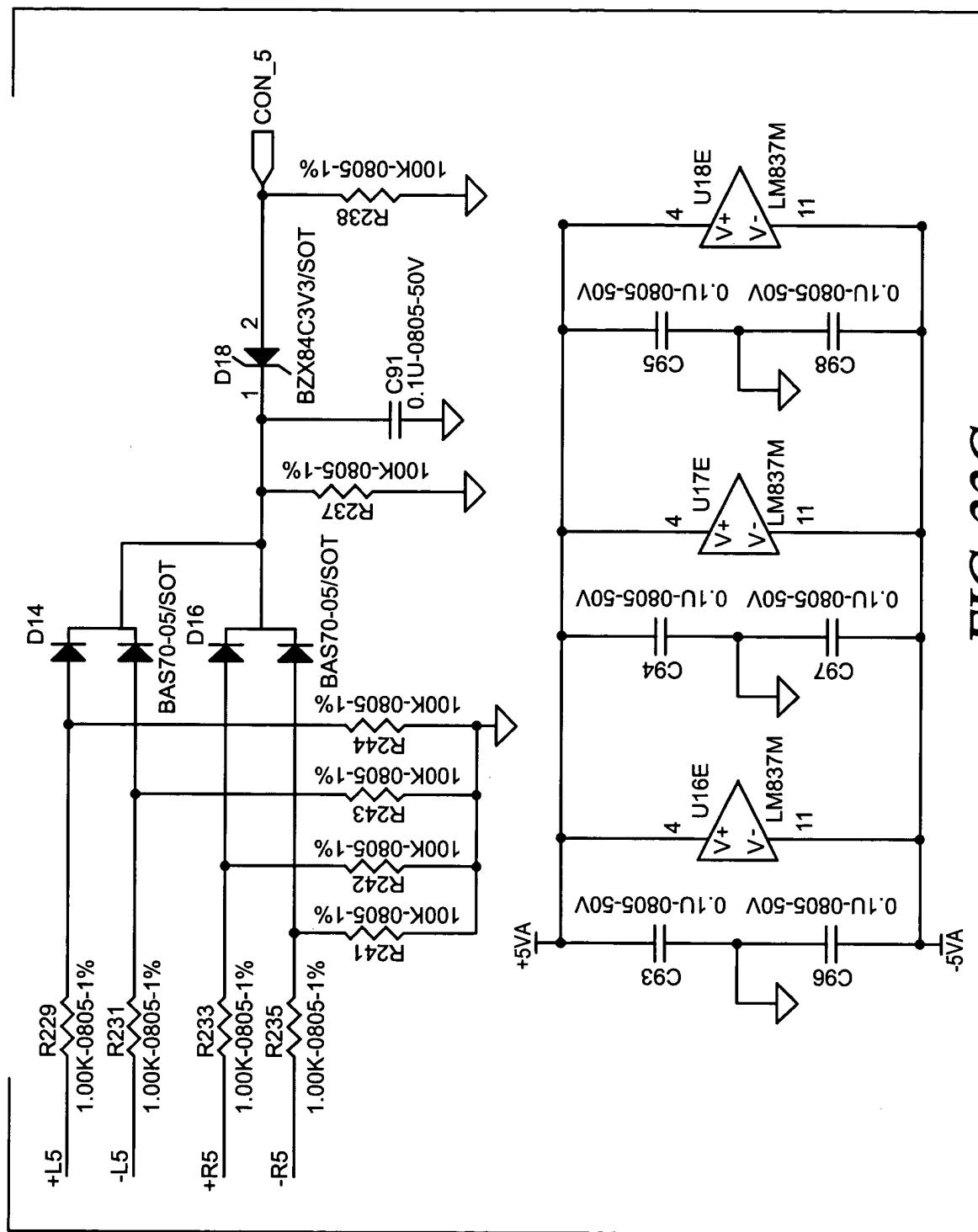


FIG. 33C

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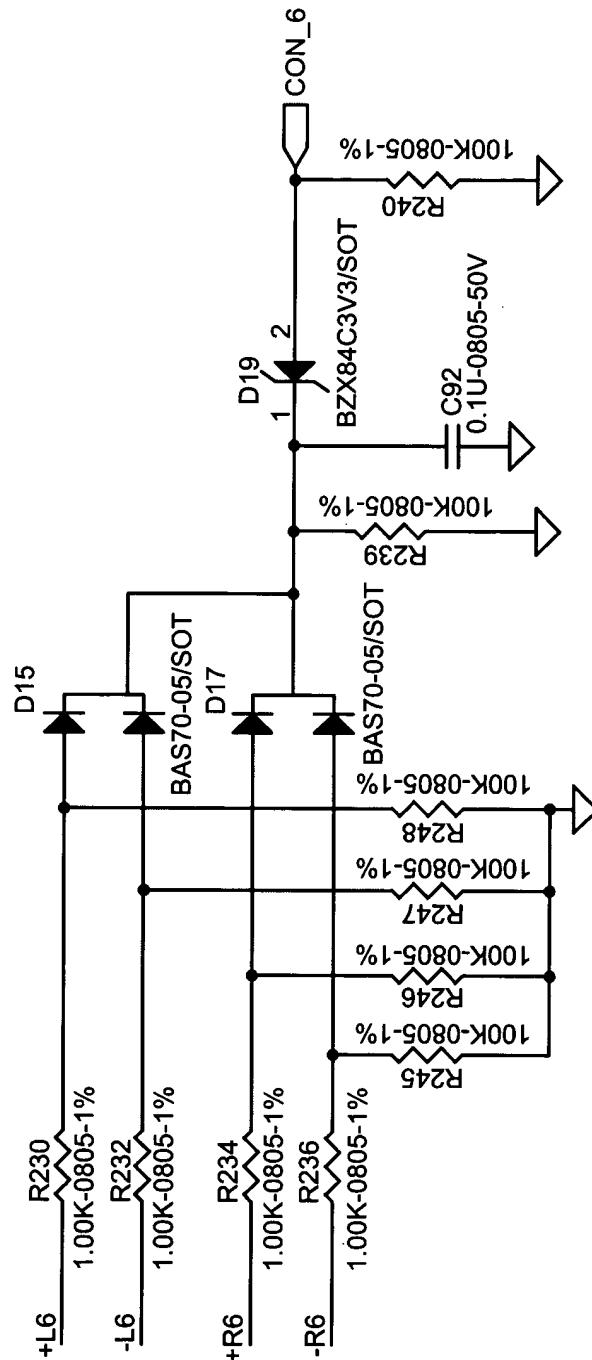


FIG. 33D

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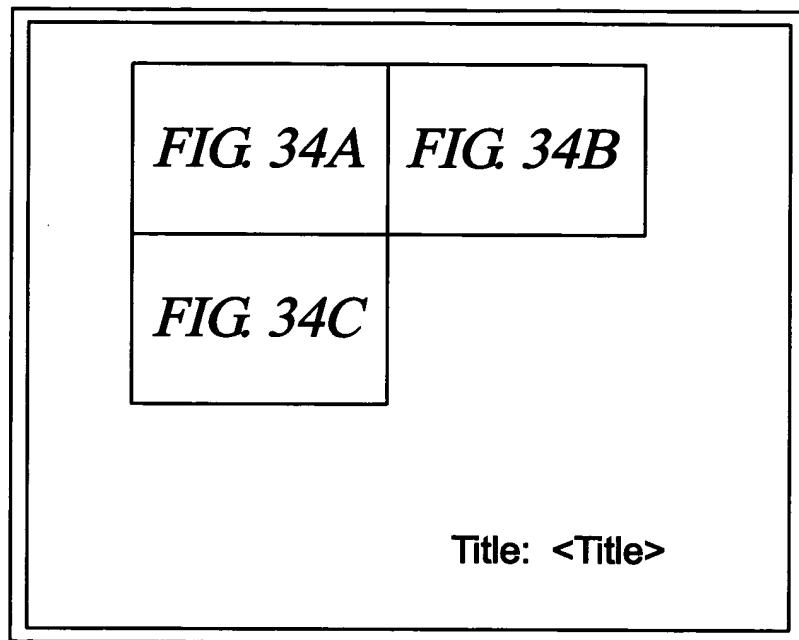


FIG. 34

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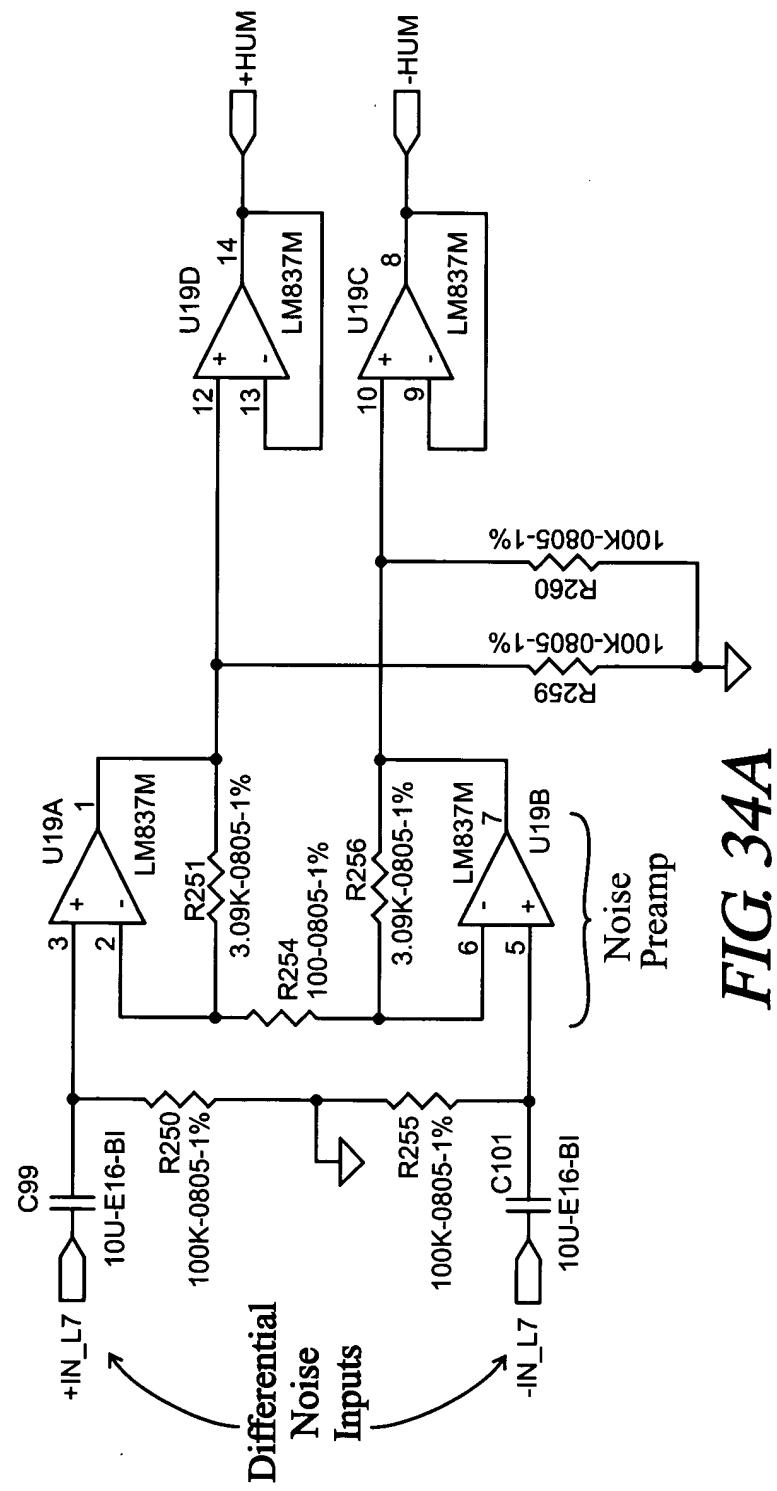


FIG. 34A

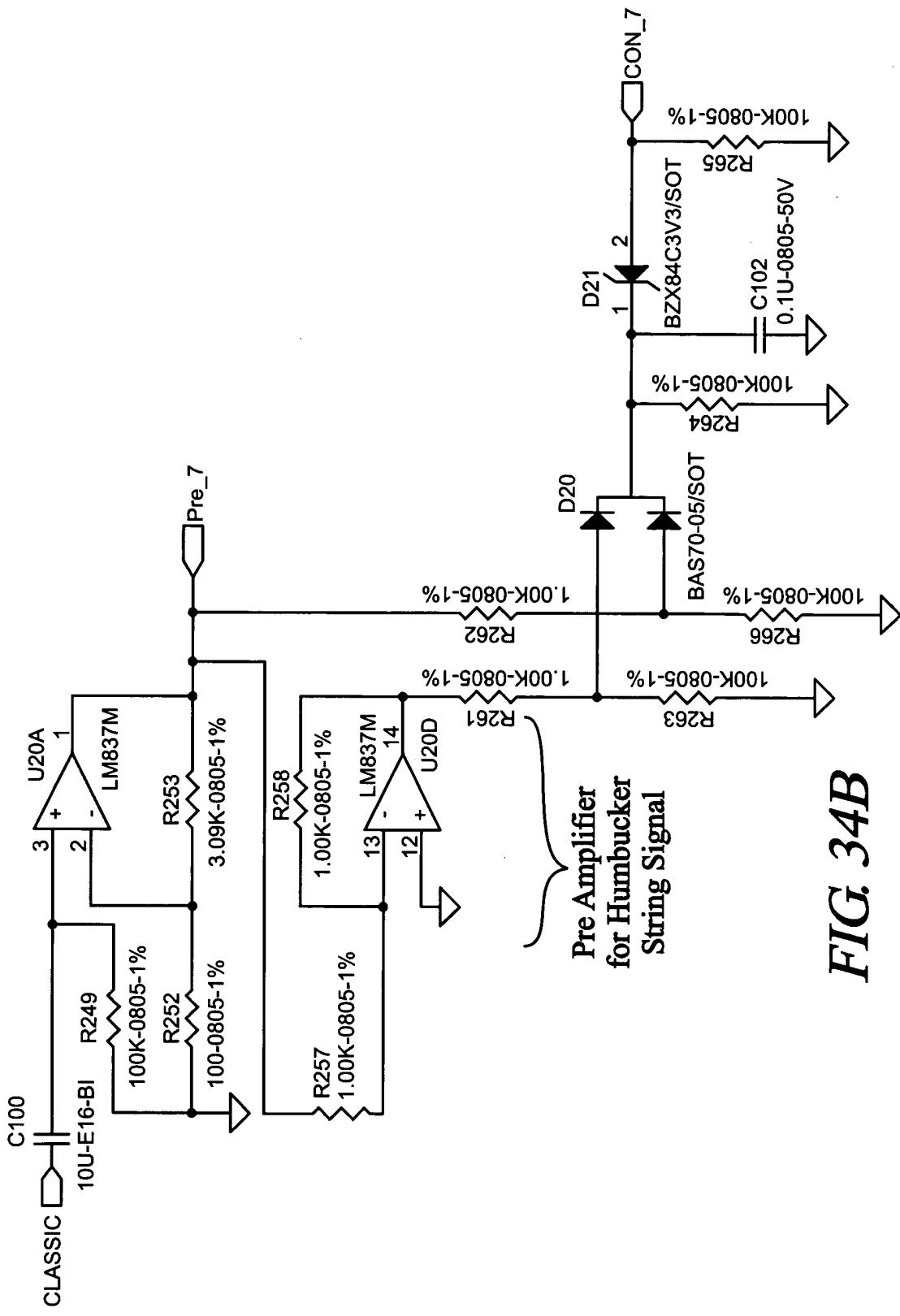


FIG. 34B

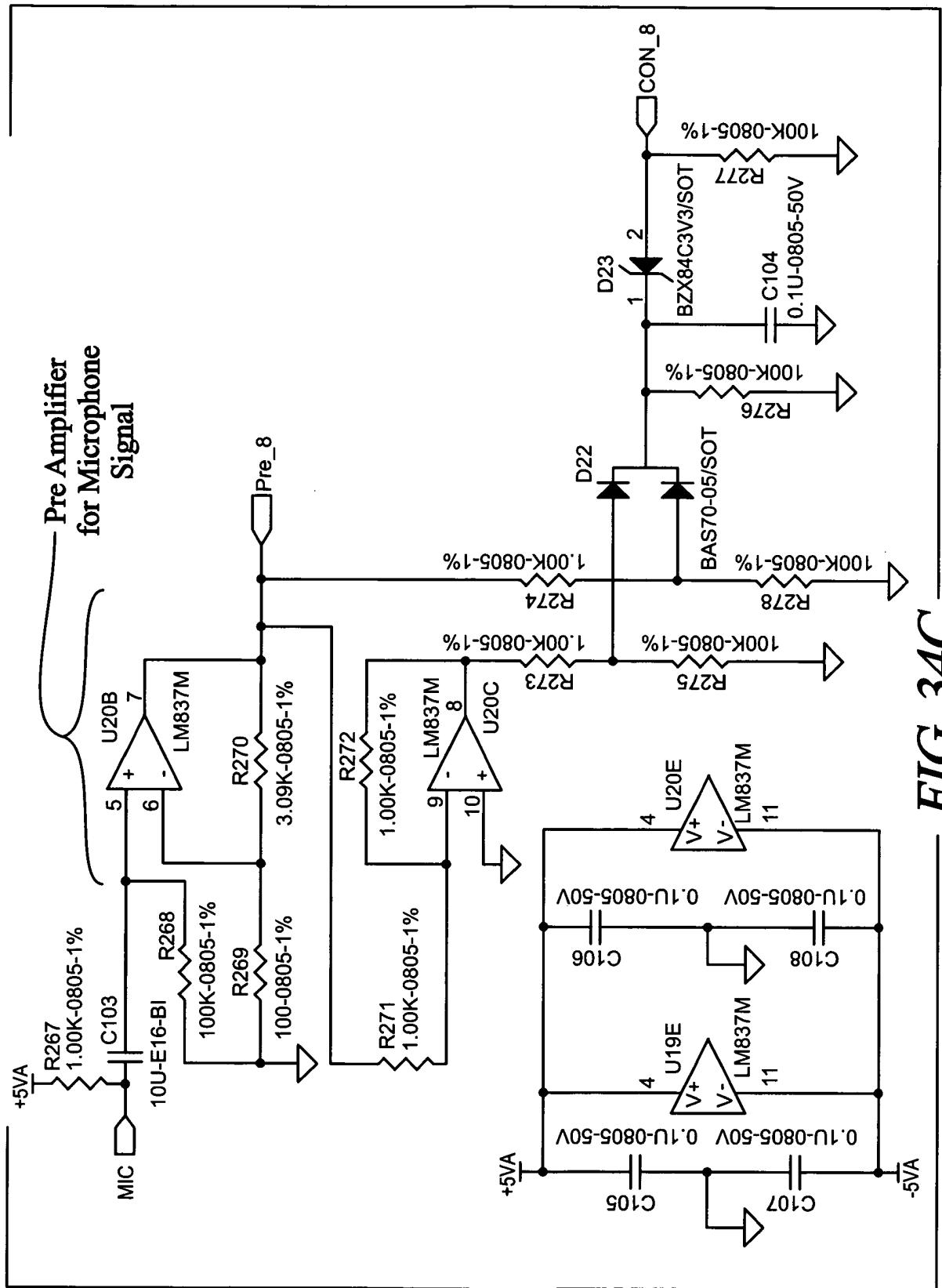


FIG. 34C.

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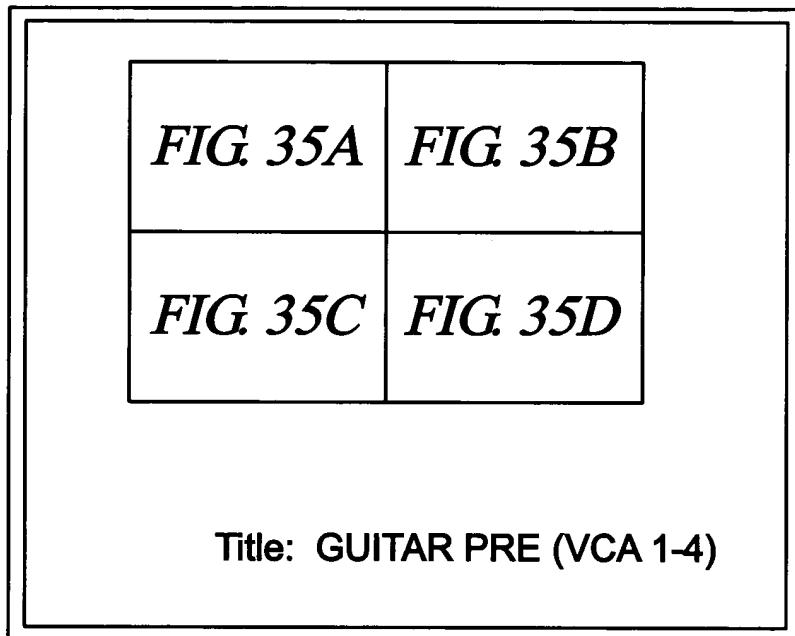


FIG 35

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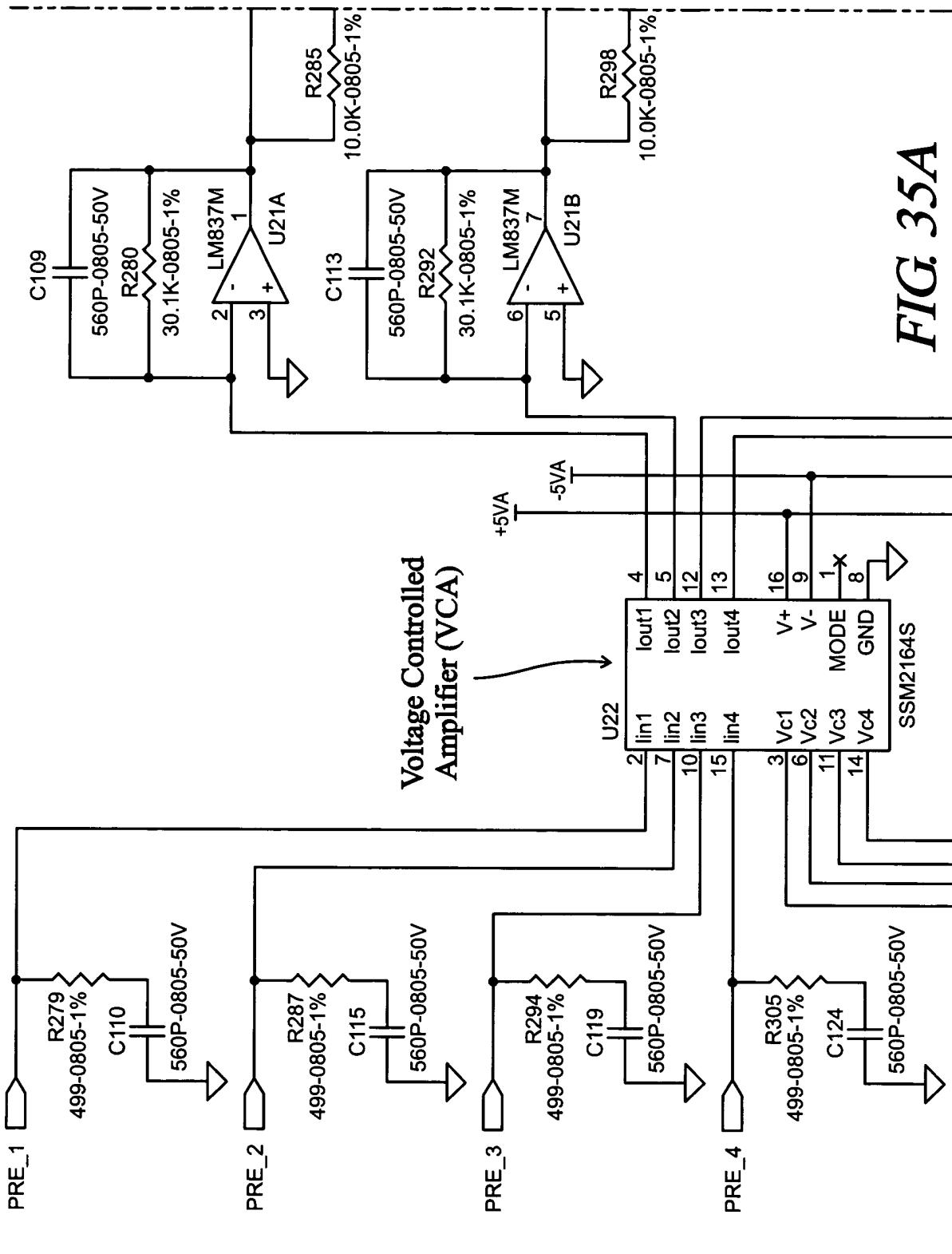


FIG. 35A

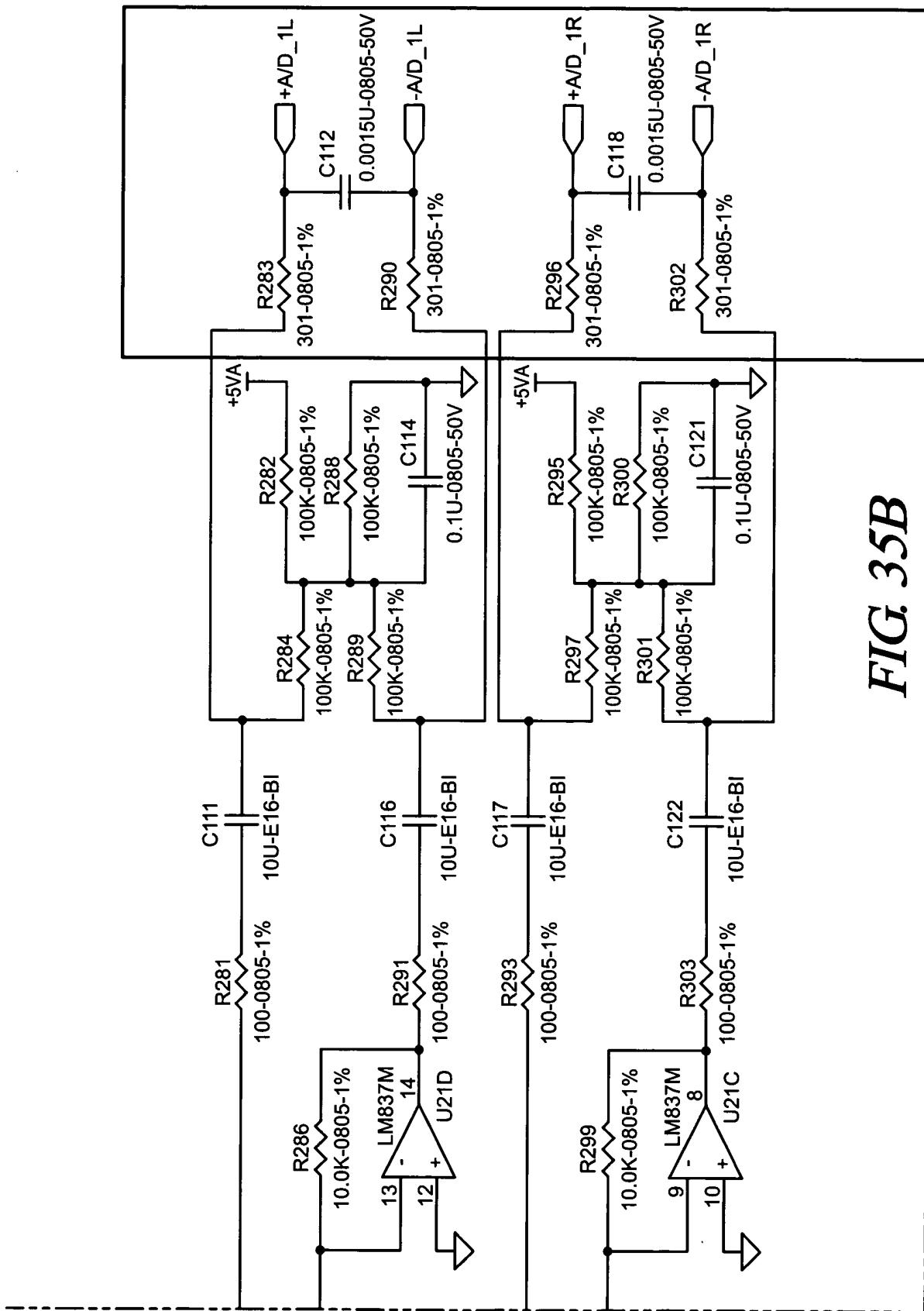
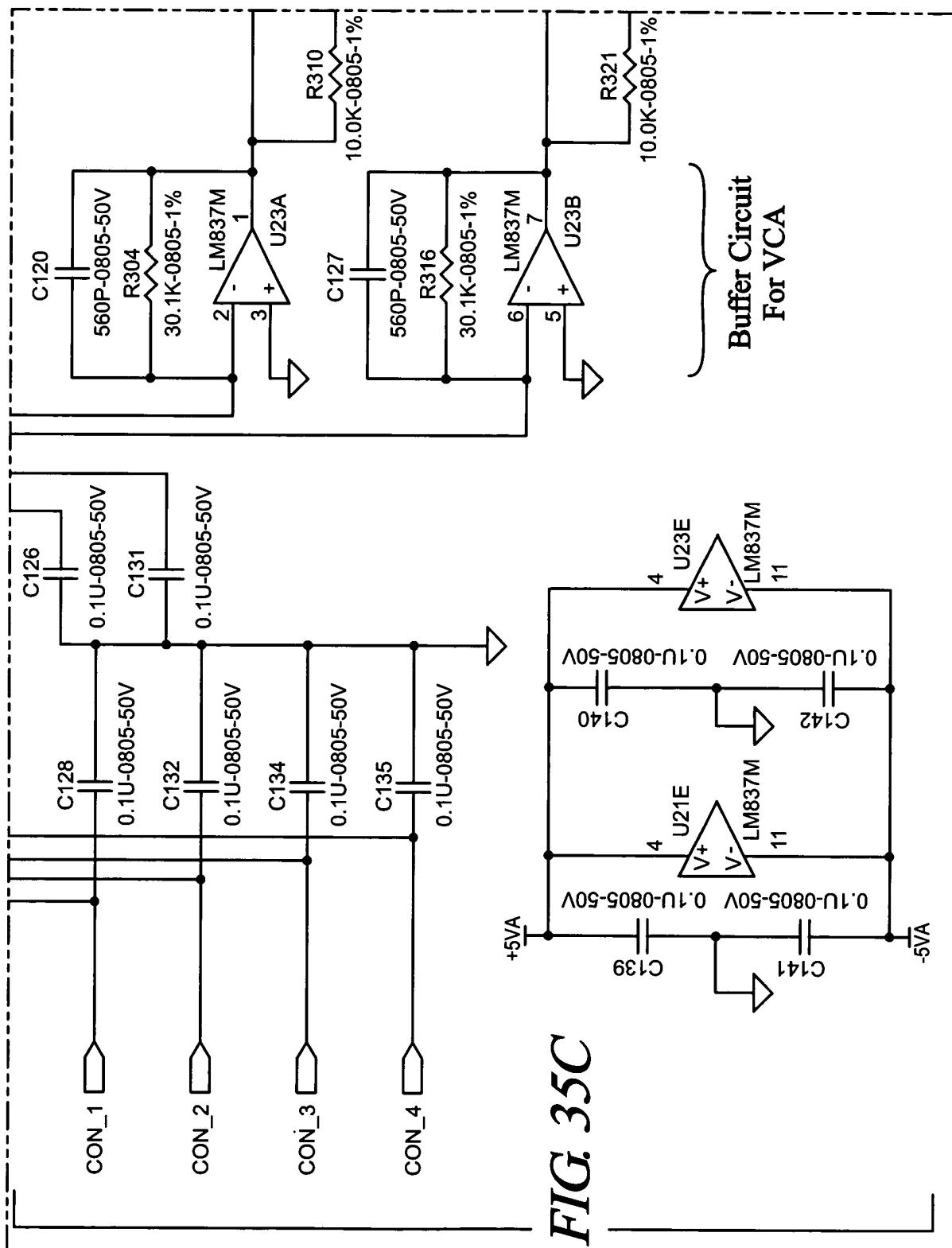


FIG. 35B



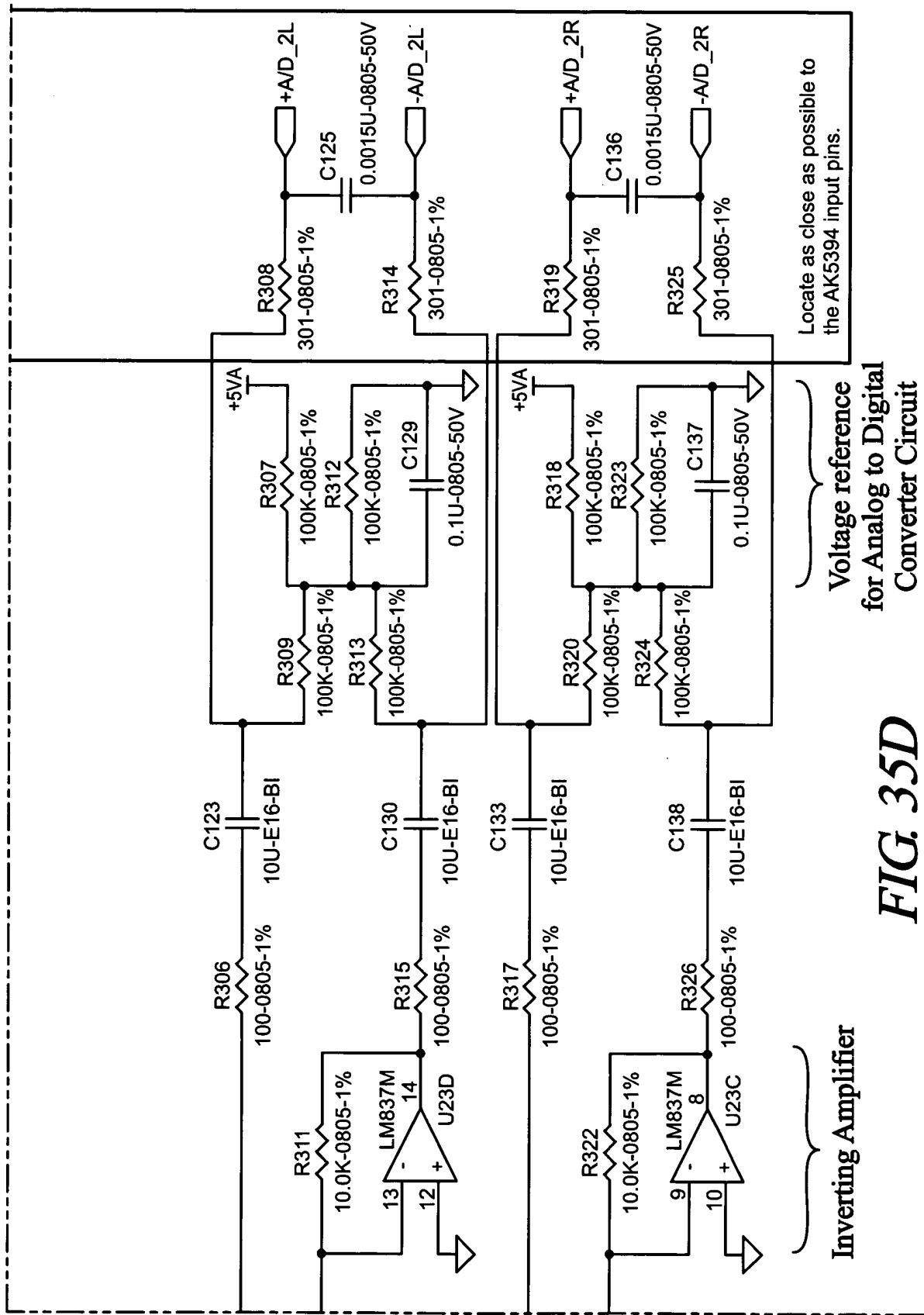


FIG. 35D

Inverting Amplifier

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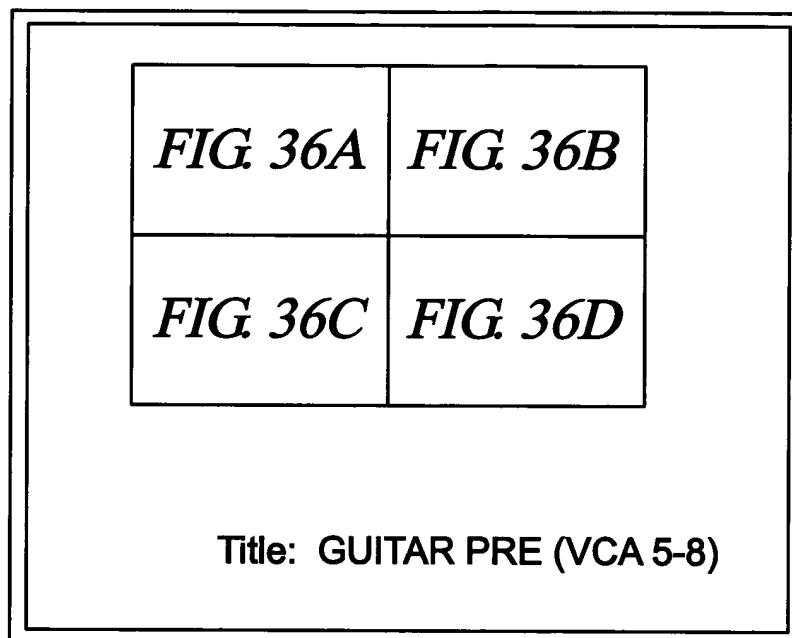


FIG. 36

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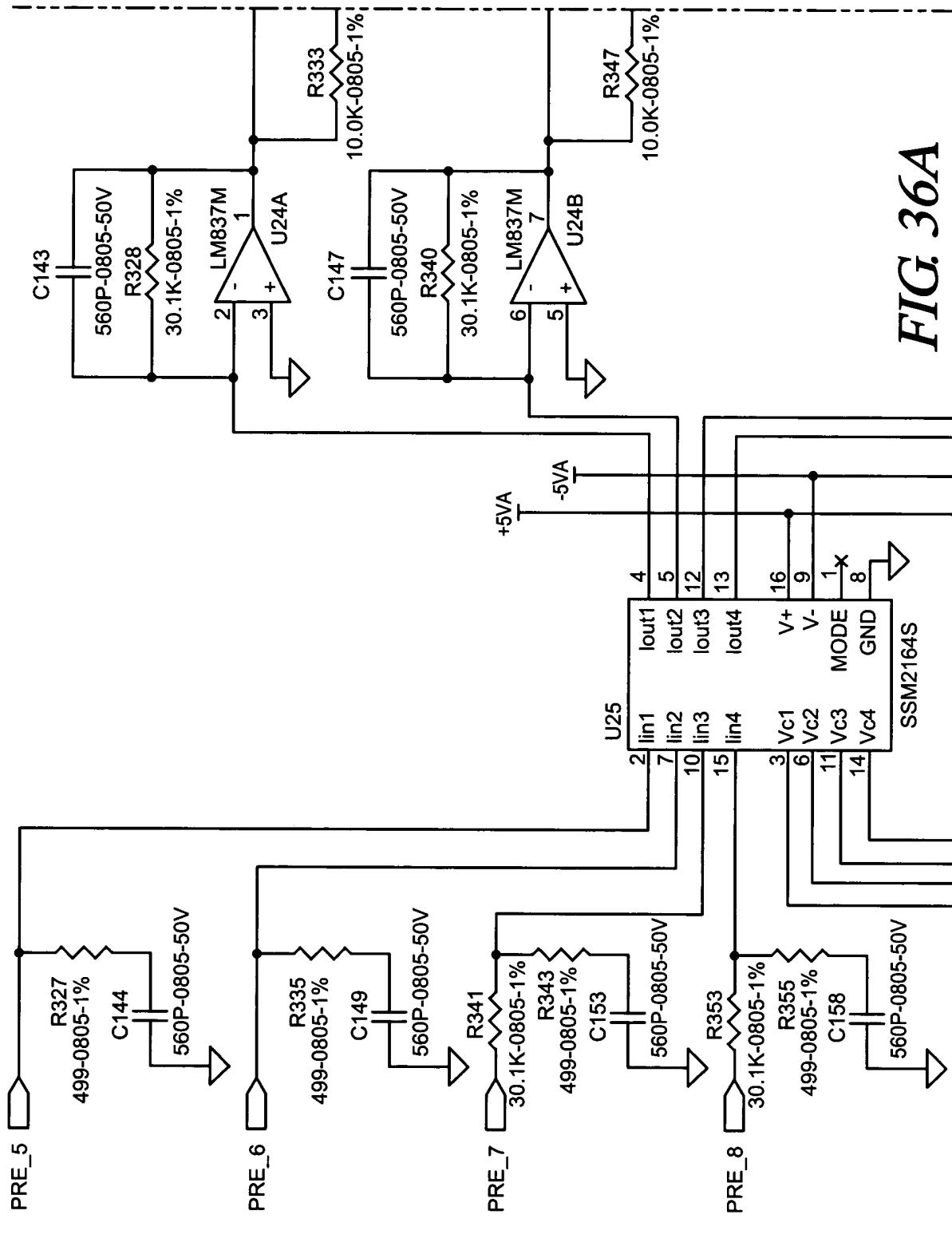


FIG 36A

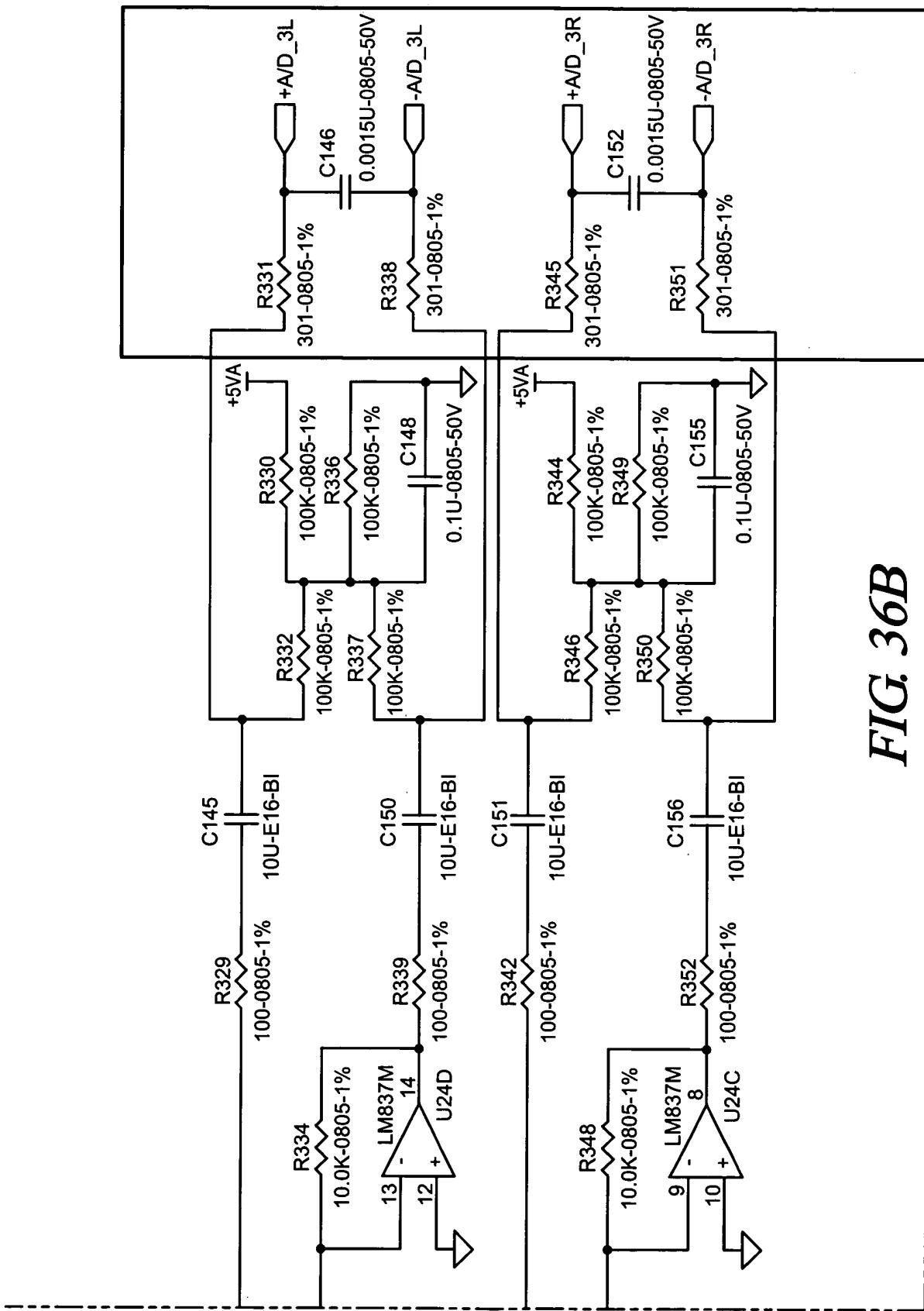


FIG 36B

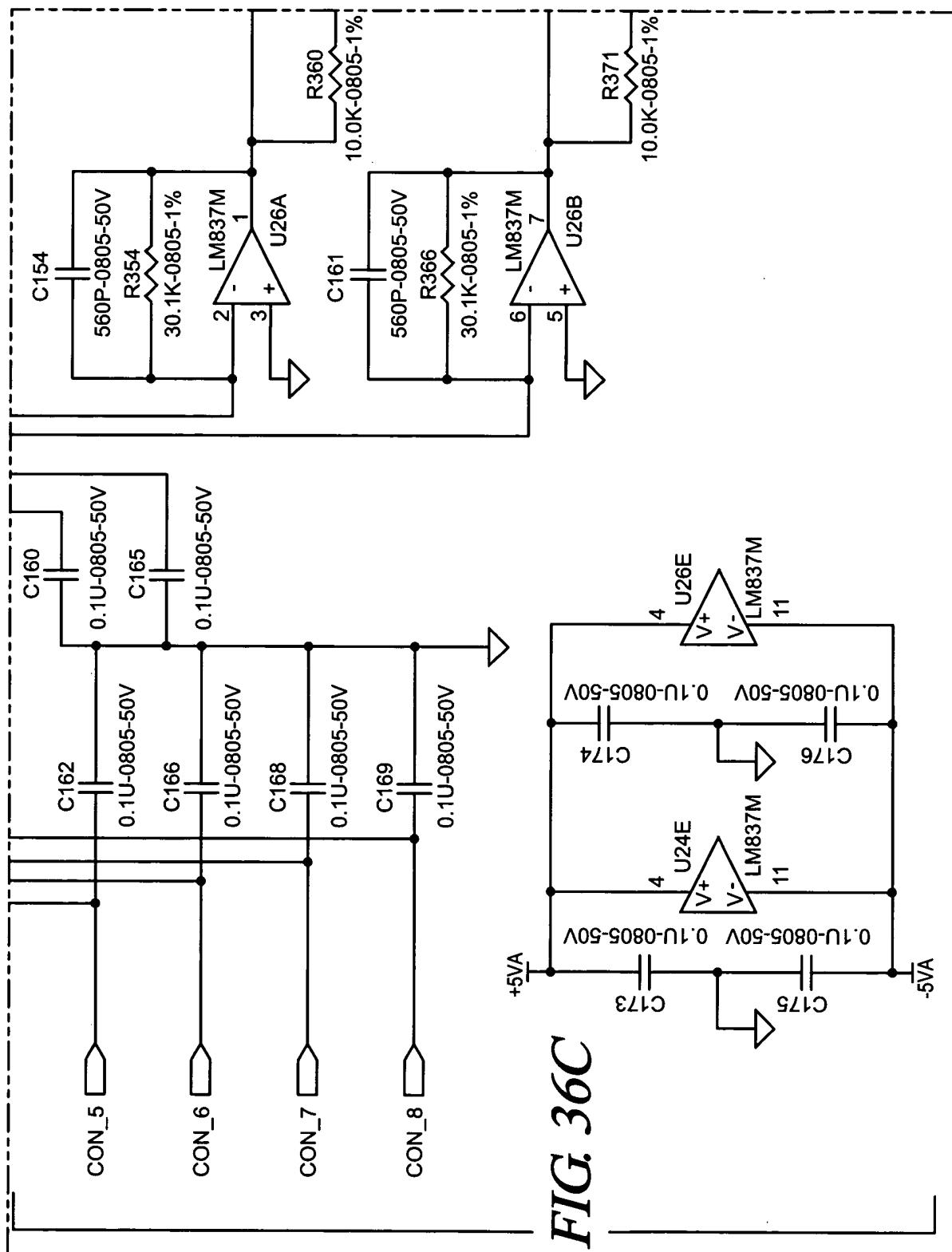


FIG. 36C

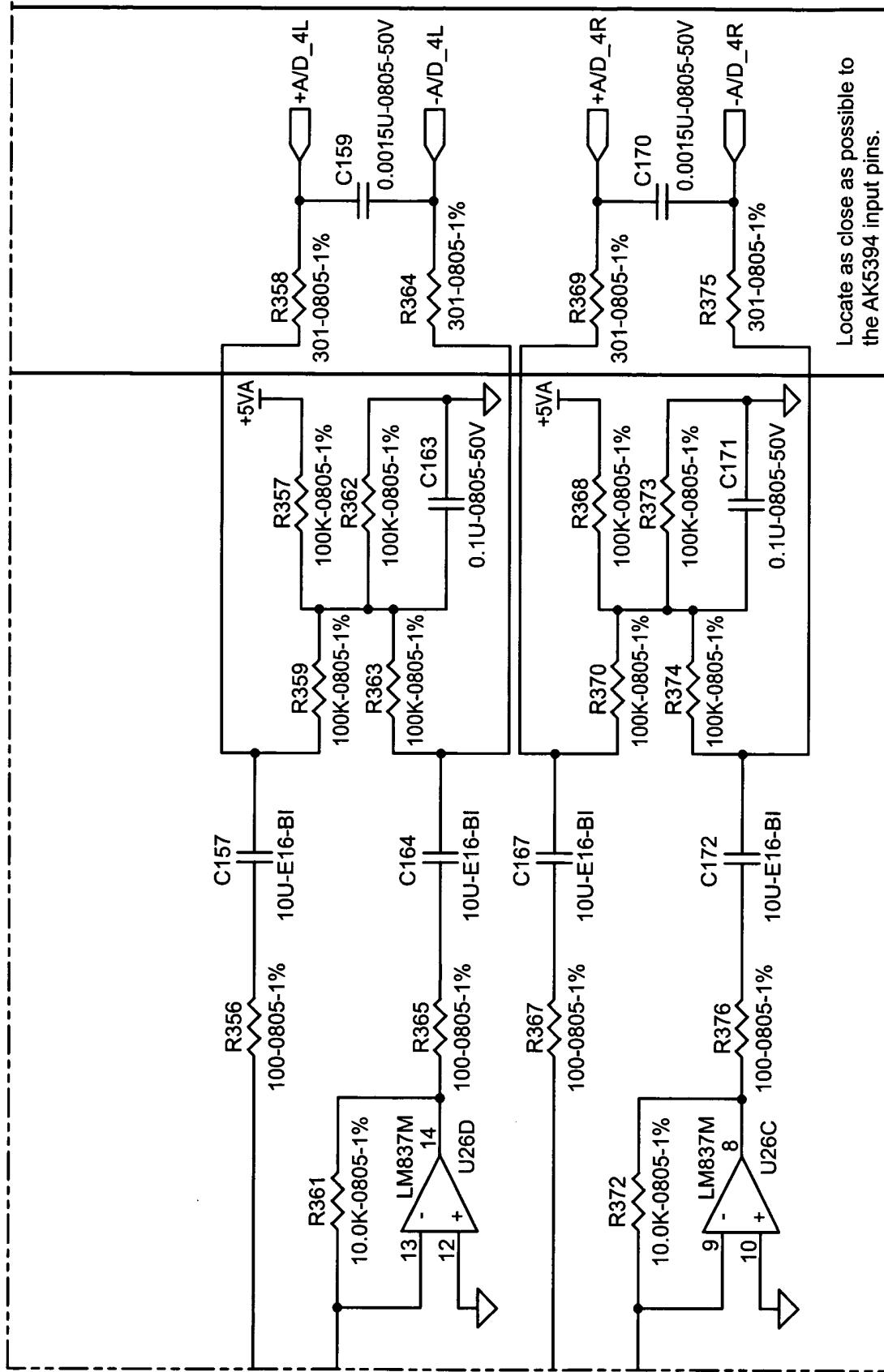


FIG. 36D